

1. Record Nr.	UNINA990002616160403321
Autore	Prosperetti, Luigi
Titolo	Commercio e sviluppo economico : un'analisi macroeconomica / Luigi Prosperetti
Pubbl/distr/stampa	Milano : Franco Angeli, 1986
Descrizione fisica	174 p. ; 22 cm
Locazione	ECA SE S DTE
Collocazione	4-8-447-TI O/3.1 PRO XV H1 114
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia

2.	Record Nr.	UNISOBSOBE00068670
	Autore	Colli, Giorgio
	Titolo	3: Eraclito / Giorgio Colli
	Pubbl/distr/stampa	Milano, : Adelphi, 1980
	Descrizione fisica	215 p. ; 23 cm
	Lingua di pubblicazione	Italiano
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
3.	Record Nr.	UNINA9910450999803321
	Titolo	Communicating process architectures 2005 [[electronic resource]] : WoTUG-28 : proceedings of the 28th WoTUG Technical Meeting, 18-21 September 2005, Technische Universiteit Eindhoven, The Netherlands / / ed. by Jan F. Broenink ... [et al.]
	Pubbl/distr/stampa	Amsterdam ; ; Washington, D.C., : IOS Press, c2005
	ISBN	1-280-50507-9 9786610505074 1-4237-9735-3 1-60750-144-9 600-00-0366-8 1-60129-124-8
	Descrizione fisica	1 online resource (416 p.)
	Collana	Concurrent systems engineering series, , 1383-7575 ; ; v. 63
	Altri autori (Persone)	BroeninkJan F
	Disciplina	004.35
	Soggetti	Parallel processing (Electronic computers) occam (Computer program language) Transputers Computer architecture Electronic books.
	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia

Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	<p>Title page; Preface; Programme Committee; Contents; Interfacing with Honeysuckle by Formal Contract; Groovy Parallel! A Return to the Spirit of occam?; On Issues of Constructing an Exception Handling Mechanism for CSP-Based Process-Oriented Concurrent Software; Automatic Handel-C Generation from MATLAB and Simulink for Motion Control with an FPGA; JCSP-Poison: Safe Termination of CSP Process Networks; jcsp.mobile: A Package Enabling Mobile Processes and Channels; CSP++: How Faithful to CSPm?; Fast Data Sharing within a Distributed, Multithreaded Control Framework for Robot Teams Improving TCP/IP Multicasting with Message SegmentationLazy Cellular Automata with Communicating Processes; A Unifying Theory of True Concurrency Based on CSP and Lazy Observation; The Architecture of the Minimum intrusion Grid (MiG); Verification of JCSP Programs; Architecture Design Space Exploration for Streaming Applications through Timing Analysis; A Foreign-Function Interface Generator for occam-pi; Interfacing C and occam-pi; Interactive Computing with the Minimum intrusion Grid (MiG); High Level Modeling of Channel-Based Asynchronous Circuits Using Verilog</p> <p>Mobile Barriers for occam-pi: Semantics, Implementation and ApplicationException Handling Mechanism in Communicating Threads for Java; R16: A New Transputer Design for FPGAs; Towards Strong Mobility in the Shared Source CLI; gCSP occam Code Generation for RMoX; Assessing Application Performance in Degraded Network Environments: An FPGA-Based Approach; Communication and Synchronization in the Cell Processor (Invited Talk); Homogeneous Multiprocessing for Consumer Electronics (Invited Talk); Handshake Technology: High Way to Low Power (Invited Talk)</p> <p>If Concurrency in Software Is So Simple, Why Is It So Hard? (Invited Talk)</p> <p>Author Index</p>
Sommario/riassunto	<p>Modern computing systems work when all components are correct by design and can be combined to achieve scalability. This publication offers refereed papers covering various aspects such as: system design and implementation; tools (concurrent programming languages, libraries, and run-time kernels); and, formal methods and applications.</p>