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| 1. Record Nr. | UNISOBE600200031402 |
| Autore | Köhnlechner, Manfred |
| Titolo | Progetti realizzabili / Manfred Köhnlechner ; trad. Francesco Saba Sardi |
| Pubbl/distr/stampa | Milano : Mondadori, 1976 |
| Descrizione fisica | 264 p. ; 21 cm |
| Lingua di pubblicazione | Italiano |
| Formato | Materiale a stampa |
| Livello bibliografico | Monografia |
| 2. Record Nr. | UNISALENTO991003215689707536 |
| Autore | European Conference on the History of Economics <4. : 1998> |
| Titolo | Economics and interdisciplinary exchange / edited by Guido Erreygers |
| Pubbl/distr/stampa | London ; New York : Routledge, 2001 |
| ISBN | 0415224454 |
| Descrizione fisica | xvi, 214 p. ; 23 cm. |
| Collana | Routledge studies in the history of economics ; 35 |
| Altri autori (Persone) | Erreygers, Guido |
| Disciplina | 330 |
| Soggetti | Economia - Congressi |
| Lingua di pubblicazione | Inglese |
| Formato | Materiale a stampa |
| Livello bibliografico | Monografia |

3. Record Nr.	UNINA9910954160303321
Titolo	Circuit analysis // Virginia E. Wright, editor
Pubbl/distr/stampa	Hauppauge, N.Y., : Nova Science Publishers, c2011
ISBN	1-61728-703-2
Edizione	[1st ed.]
Descrizione fisica	1 online resource (196 p.)
Collana	Electrical engineering developments
Altri autori (Persone)	WrightVirginia E
Disciplina	621.3815
Soggetti	Electronic circuits Electric circuit analysis
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Intro -- CIRCUIT ANALYSIS -- CIRCUIT ANALYSIS -- LIBRARY OF CONGRESS CATALOGING-IN-PUBLICATION DATA -- CONTENTS -- PREFACE -- Chapter 1: ELEMENT STAMP ALGORITHM FOR MATRIX FORMULATION OF SYMBOLIC CIRCUITS -- Abstract -- 1. Computer Representation of Symbolic Matrices -- 2. Topological (Graphical) Representation of Matrices -- 3. Basic Network Elements and Equations (Distributed vs. Lumped) -- 4. Nodal Analysis Techniques -- 5. Conclusion -- References -- Chapter 2: MICROWAVE AND MILLIMETER-WAVE COAXIAL-WAVEGUIDE POWERDIVIDING/ COMBINING CIRCUITS -- Abstract -- 1. Introduction -- 2. Coaxial-Waveguide Power-Dividing/Combining Structure -- 3. Design of the Coaxial Stepped Impedance Transformer -- 4. Analysis of the Coaxial Taper -- 5. Coaxial-Waveguide Power Dividers/ Combiners Using Coaxial Probe Array -- 6. Coaxial-Waveguide Power Dividers with Microstrip Probes -- 7. Millimeter-Wave Coaxial-Waveguide Power-Combining Amplifiers -- 8. Planar Probe Coaxial Power Dividers/Combiners -- 9. Ultra-Wideband (UWB) Coaxial Power Divider with Rotated Electric Field Mode -- References -- Chapter 3: ANALYSIS OF FEEDBACK CIRCUITS USING MILLER'S TECHNIQUES -- Abstract -- I. Introduction -- II. Open Circuit Approximation Using Circuit Properties: Parallel Feedback Impedance -- III. Determination of All Parameters: Parallel Feedback -- IV. Series Feedback Impedance: Short Circuit Approximation and Analysis -- V. Conclusions -- References -- Chapter 4 TIRE PRESSURE -TEMPERATURE TESTER FOR REAL DRIVING CONDITIONS -- Abstract -- Introduction --

Experimental Data Presentation and Discussion -- Conclusion --
References -- Chapter 5: ANALYZING CIRCUIT STRUCTURES AS
LANGUAGE -- Abstract -- 1. Introduction -- 2. Circuit Representation
in Predicate Logic -- 3. Logic Grammar DCSG -- 4. Finding Structures
in Circuits -- 5. Circuit Grammar for Functional Blocks.
6. Grammar Rules for Functional Blocks -- 7. Parsing Circuits -- 8.
Conclusions -- References -- INDEX.

Sommario/riassunto

Presents a method for analysing circuit structures as sentences and
their elements as words in order to increase the level of automatic
circuit understanding.
