

1.	Record Nr.	UNISALENTO991004075619707536
	Autore	Bonassies, Jules
	Titolo	La commédie française et le commédiens de province aux XVII et XVIII siècles / Jules Bonassies
	Pubbl/distr/stampa	Genève : Slatkine, 1974
	Descrizione fisica	60 p. ; 22 cm
	Disciplina	842.409
	Soggetti	Teatro francese - Studi
	Lingua di pubblicazione	Francese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
2.	Record Nr.	UNISALENTO991000447309707536
	Autore	Bubner, Rüdiger
	Titolo	Dialektik als topik : bausteine zu einer lebensweltlichen theorie der rationalität / Rüdiger Bubner
	Pubbl/distr/stampa	Frankfurt am Main : Suhrkamp, c1990
	ISBN	351811591X
	Descrizione fisica	111 p. ; 18 cm
	Collana	Edition Suhrkamp. Neue Folge ; 591
	Disciplina	160
	Soggetti	Ermeneutica Filosofia contemporanea Filosofia moderna
	Lingua di pubblicazione	Tedesco
	Formato	Materiale a stampa
	Livello bibliografico	Monografia

3. Record Nr.	UNINA9910372751403321
Autore	Kletter Raz
Titolo	Archaeology, heritage and ethics in the Western Wall Plaza, Jerusalem : darkness at the end of the tunnel // Raz Kletter
Pubbl/distr/stampa	2019 London ; ; New York : , : Routledge, Taylor & Francis Group, , 2020 ©2020
ISBN	0-429-03131-9 0-429-63197-9 0-429-63346-7
Edizione	[First edition.]
Descrizione fisica	1 online resource (363 pages)
Collana	Copenhagen international seminar
Classificazione	HIS002000
Disciplina	956.9442
Soggetti	Excavations (Archaeology) - West Bank Excavations (Archaeology) - Jerusalem
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	1. Journey to East Jerusalem: an introduction -- 2: Antiquities in the toilettes: the Strauss Building -- 3. Wild Western Wall tunnels: the Davidson Centre and the Archaeological Park -- 4. A museum for Jewish prayer in a Mamluk bathhouse: the Ohel Yizthak Synagogue -- 5. An archaeological site with depth: the ha-Liba Building -- 6. Throwing dust in the eyes: the comprehensive plan for the Western Wall Plaza -- 7. Lingua Orientalis Hierosolimitanae -- 8. Pilegsh at Givati: little Tel Aviv in East Jerusalem -- 9. The ethics of East Jerusalem -- 10. Conclusions.
Sommario/riassunto	"This volume is a critical study of recent archaeology in the Western Wall Plaza area, Jerusalem. Considered one of the holiest places on Earth for Jews and Muslims, it is also a place of controversy, where the State marks 'our' remains for preservation and adoration and 'theirs' for silencing. Based on thousands of documents from the Israel Antiquities Authority and other sources, such as protocols of planning committees, readers can explore for the first time this archaeological 'heart of darkness' in East Jerusalem. The book follows a series of unique discoveries, reviewing the approval and execution of development

plans and excavations, and the use of the areas once excavation has finished. Who decides what and how to excavate, what to preserve - or 'remove'? Who pays for the archaeology, for what aims? The professional, scientific archaeology of the past happens now: it modifies the present and is modified by it. This book 'excavates' the archaeology of East Jerusalem to reveal its social and political contexts, power structures and ethics. Readers interested in the history, archaeology and politics of the Israeli-Palestinian conflict will find this book useful, as well as scholars and students of the history and ethics of Archaeology, Jerusalem, conservation, nationalism, and heritage"--

4. Record Nr.	UNINA9910366582703321
Autore	Mehta Ashok B.
Titolo	System Verilog Assertions and Functional Coverage : Guide to Language, Methodology and Applications / / by Ashok B. Mehta
Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2020
ISBN	3-030-24737-6
Edizione	[3rd ed. 2020.]
Descrizione fisica	1 online resource (XXXIX, 507 p. 270 illus., 258 illus. in color.)
Disciplina	621.3815 621.392
Soggetti	Electronic circuits Electronics Microelectronics Microprocessors Circuits and Systems Electronics and Microelectronics, Instrumentation Processor Architectures
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Includes index.
Nota di contenuto	Introduction -- System Verilog Assertions -- Immediate Assertions -- Concurrent Assertions -- Basics (sequence, property, assert) -- Sampled Value Functions \$rose, \$fell -- Operators -- System Functions and

Tasks -- Multiple clocks -- Local Variables -- Recursive property -- Detecting and using endpoint of a sequence -- 'expect' -- 'assume' and formal (static functional) verification -- Other important topics -- Asynchronous Assertions !!! -- IEEE-1800–2009 Features -- SystemVerilog Assertions LABs -- System Verilog Assertions – LAB Answers -- Functional Coverage -- Performance Implications of coverage methodology -- Coverage Options.

Sommario/riassunto

This book provides a hands-on, application-oriented guide to the language and methodology of both SystemVerilog Assertions and Functional Coverage. Readers will benefit from the step-by-step approach to learning language and methodology nuances of both SystemVerilog Assertions and Functional Coverage, which will enable them to uncover hidden and hard to find bugs, point directly to the source of the bug, provide for a clean and easy way to model complex timing checks and objectively answer the question 'have we functionally verified everything'. Written by a professional end-user of ASIC/SoC/CPU and FPGA design and Verification, this book explains each concept with easy to understand examples, simulation logs and applications derived from real projects. Readers will be empowered to tackle the modeling of complex checkers for functional verification and exhaustive coverage models for functional coverage, thereby drastically reducing their time to design, debug and cover. This updated third edition addresses the latest functional set released in IEEE-1800 (2012) LRM, including numerous additional operators and features. Additionally, many of the Concurrent Assertions/Operators explanations are enhanced, with the addition of more examples and figures. · Covers in its entirety the latest IEEE-1800 2012 LRM syntax and semantics; · Covers both SystemVerilog Assertions and SystemVerilog Functional Coverage languages and methodologies; · Provides practical applications of the what, how and why of Assertion Based Verification and Functional Coverage methodologies; · Explains each concept in a step-by-step fashion and applies it to a practical real life example; · Includes 6 practical LABs that enable readers to put in practice the concepts explained in the book.
