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Autore Italia. Ministero di agricoltura industria e commercio

Titolo Regio decreto del 30 giugno 1907, n. 546 che dà piena ed intiera

esecuzione alla convenzione fra l'Italia e la Francia relativa al trattamento degli operai in casi d'infortunio : Gazzetta Ufficiale del 1.

agosto 1907 n. 182 / Ministero di agricoltura industria e commercio,

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Autore Wright Don (Donald P.)

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Donald P. Wright

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Record Nr. UNINA9910144126703321 Parallel and Distributed Processing [[electronic resource]]: 11th **Titolo** IPPS/SPDP'99 Workshops Held in Conjunction with the 13th International Parallel Processing Symposium and 10th Symposium on Parallel and Distributed Processing San Juan, Puerto Rico, USA, April 12-16, 1999 Proceedings / / edited by Jose Rolim Berlin, Heidelberg:,: Springer Berlin Heidelberg:,: Imprint: Springer, Pubbl/distr/stampa , 1999 3-540-48932-0 **ISBN** Edizione [1st ed. 1999.] Descrizione fisica 1 online resource (XXXIV, 1444 p. 365 illus.) Lecture Notes in Computer Science, , 0302-9743 ; ; 1586 Collana Disciplina 005.1 Soggetti Software engineering Computer architecture Computer organization Computer hardware Computer science—Mathematics Computers Software Engineering/Programming and Operating Systems Computer System Implementation Computer Systems Organization and Communication Networks Computer Hardware Discrete Mathematics in Computer Science Theory of Computation Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Note generali Bibliographic Level Mode of Issuance: Monograph Nota di contenuto Fourth International Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'99) -- Efficient program partitioning based on compiler controlled communication -- SCI-VM: A flexible base for transparent shared memory programming models on clusters of PCs -- Flexible collective operations for distributed object groups -- SCALA: A framework for performance evaluation of scalable computing -- Recursive individually distributed object -- The MuSE

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architectures workshop -- DEFACTO: A design environment for adaptive computing technology -- A web-based multiuser operating system for reconfigurable computing -- Interconnect synthesis for reconfigurable multi-FPGA architectures -- Hardwired-clusters partialcrossbar: A hierarchical routing architecture for multi-FPGA systems --Integrated block-processing and design-space exploration in temporal partitioning for RTR architectures -- Improved scaling simulation of the general reconfigurable mesh -- Bit summation on the reconfigurable mesh -- Scalable hardware-algorithms for binary prefix sums --Configuration sequencing with self configurable binary multipliers --Domain specific mapping for solving graph problems on reconfigurable devices -- MorphoSys: a reconfigurable processor targeted to high performance image application -- An efficient implementation method of fractal image compression on dynamically reconfigurable architecture -- Plastic cell architecture: A dynamically reconfigurable hardware-based computer -- Leonardo and discipulus simplex: --Reusable internal hardware templates -- An on-line arithmetic-based reconfigurable neuroprocessor -- The re-configurable delayinsensitive Flysig architecture -- Digital signal processing with general purpose microprocessors. DSP and reconfigurable logic -- Solving satisfiability problems on FPGAs using experimental unit propagation heuristic -- FPGA implementation of modular exponentiation --Workshop on Java for Parallel and Distributed Computing -- More efficient object serialization -- A customizable implementation of RMI for high performance computing -- mpiJava: An object-oriented java interface to MPI -- An adaptive, fault-tolerant implementation of BSP for Java-based volunteer computing systems -- High performance computing for the masses -- Process networks as a high-level notation for metacomputing -- Developing parallel applications using the JavaPorts environment -- 3rd workshop on Optics and Computer Science Message from the Program Chairs -- Permutation routing in all-optical product networks -- NWCache: Optimizing disk accesses via an optical network/write cache hybrid -- NetCache: A network/cache hybrid for multiprocessors -- A multi-wavelength optical contentaddressable parallel processor (MW-OCAPP) for high-speed parallel relational database processing: Architectural concepts and preliminary experimental system -- Optimal scheduling algorithms in WDM optical passive star networks -- OTIS-Based multi-hop multi-OPS lightwave networks -- Solving graph theory problems using reconfigurable pipelined optical buses -- High speed, high capacity bused interconnects using optical slab waveguides -- A new architecture for multihop optical networks -- Pipelined versus non-pipelined traffic scheduling in unidirectional WDM rings -- Irregular '99 Sixth International Workshop on Solving Irregularly Structured Problems in Parallel -- Self-Avoiding walks over adaptive unstructured grids -- A graph based method for generating the fiedler vector of irregular problems -- Hybridizing nested dissection and halo approximate minimum degree for efficient sparce matrix ordering -- ParaPART: Parallel mesh partitioning tool for distributed systems -- Sparse computations with Pei -- Optimizing irregular HPF applications using halos -- From EARTH to HTMT: An evolution of a multiheaded architecture model -- Irregular parallel algorithms in Java -- A simple framework to calculate the reaching definition of array references and its use in subscript array analysis -- Dynamic process composition and communication patterns in irregularly structured applications --Scalable parallelization of harmonic balance simulation -- A range minima parallel algorithm for coarse grained multicomputers --

behavior via speculative data distribution -- 6th reconfigurable

Deterministic branch-and-bound on distributed memory machines --2nd Workshop on Personal Computer Based Networks of Workstations (PC-NOW'99) -- Performance results for a reliable low-latency cluster communication protocol -- Coscheduling through synchronized scheduling servers—A prototype and experiments -- Highperformance knowledge extraction from data on PC-based networks of workstations -- Addressing communication latency issues on clusters for fine grained asynchronous applications—A case study -- Low cost databases for NOW -- Implementation and evaluation of MPI on an SMP cluster -- Fourth International Workshop on Formal Methods for Parallel Programming: Theory and Applications FMPPTA'99 April 16 1998 -- From a specification to an equivalence proof in objectoriented parallelism -- Examples of program composition illustrating the use of universal properties -- A formal framework for specifying and verifying time warp optimizations -- Verifying end-to-end protocols using induction with CSP/FDR -- Mechanical verification of a garbage collector -- A structured approach to parallel programming: Methodology and models -- BSP in CSP: Easy as ABC -- 4th International Workshop on Embedded HPC Systems and Applications (EHPC'99) -- A distributed system reference architecture for adaptive QoS and resource management -- Transparent real-time monitoring in MPI -- DynBench: A dynamic benchmark suite for distributed real-time systems -- Reflections on the creation of a real-time parallel benchmark suite -- Tailor-made operating systems for embedded parallel applications -- Fiber-optic interconnection networks for signal processing applications -- Reconfigurable parallel sorting and load balancing: HeteroSo.

## Sommario/riassunto

This book constitutes the refereed proceedings of 11 IPPS/SPDP '98 Workshops held in conjunction with the 13th International Parallel Processing Symposium and the 10th Symposium on Parallel and Distributed Processing in San Juan, Puerto Rico, USA in April 1999. The 126 revised papers presented were carefully selected from a wealth of papers submitted. The papers are organised in topical sections on biologically inspired solutions to parallel processing problems: High-Level Parallel Programming Models and Supportive Environments; Biologically Inspired Solutions to Parallel Processing; Parallel and Distributed Real-Time Systems; Run-Time Systems for Parallel Programming; Reconfigurable Architectures; Java for Parallel and Distributed Computing; Optics and Computer Science; Solving Irregularly Structured Problems in Parallel; Personal Computer Based Workstation Networks; Formal Methods for Parallel Programming; Embedded HPC Systems and Applications.

Record Nr. UNINA9910348229903321 Autore Semmler Willi Titolo Monetary policy and unemployment: the US, Euro-area and Japan // edited by Willi Semmler London, : Routledge, 2005 Pubbl/distr/stampa **ISBN** 9786610289691 9781135993801 1135993807 9781280289699 1280289694 9780203329580 0203329589 Edizione [1st ed.] Descrizione fisica 1 online resource (288 p.) Collana Routledge international studies in money and banking;; 31 Classificazione 83.44 83.61 QK 900 **QV 202** Disciplina 331.137 Soggetti Monetary policy - Europe Monetary policy - United States Monetary policy - Japan Unemployment - Europe

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6 Labor market institutions and unemployment in Europe: A comment on Blanchard and Wolfers7 Labor market dynamics in the Euro-area: A model-based sensitivity analysis; Part III Structuralist causes of unemployment and monetary policy; 8 The structuralist perspective on real exchange rate, share price level, and employment path: What room is left for money?; 9 The long stagnation and monetary policy in Japan: A theoretical explanation; 10 Monetary policy, the labor market, and pegged exchange rates: A study of the German economy; Part IV Monetary policy rules, fiscal policy, and unemployment 11 The constitutional position of the Central Bank12 Activist stabilization policy and inflation: The Taylor rule in the 1970s; 13 The Fed's monetary policy rule: Past, present, and future; 14 What is the impact of tax and welfare reforms on fiscal stabilizers?: A simple model and an application to EMU; Index

## Sommario/riassunto

This book pulls together papers presented at a conference in honour of the 1981 Nobel Prize Winner for Economic Science, the late James Tobin. Among the contributors are Olivier Blanchard, Edmund Phelps, Charles Goodhart and Marco Buti. One of the main aims of the conference was to discuss what potential role monetary policy has on economic activity and unemployment reduction in three key currency zones - the United States, European Union and Japan.