

1.	Record Nr.	UNISALENTO991000827199707536
	Autore	Finzi, Bruno
	Titolo	Dinamica / Bruno Finzi
	Pubbl/distr/stampa	Bologna : Zanichelli, 1970
	Edizione	[3. ed]
	Descrizione fisica	xi, 427 p. ; 25 cm.
	Collana	Meccanica razionale ; 2
	Classificazione	AMS 70-XX
	Disciplina	510
	Soggetti	Mechanics-textbooks
	Lingua di pubblicazione	Italiano
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	Livello bibliografico	Monografia
2.	Record Nr.	UNINA9910147236903321
	Titolo	IEEE Standard VHDL Language Reference Manual
	Pubbl/distr/stampa	[Place of publication not identified], : IEEE, 2002
	ISBN	0-7381-3248-9
	Descrizione fisica	1 online resource
	Disciplina	621.3819
	Soggetti	VHDL (Computer hardware description language) - Standards VHDL (Computer hardware description language) Computer hardware description languages Engineering - Computer programs
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Sommario/riassunto

VHSIC Hardware Description Language (VHDL) is defined. VHDL is a formal notation intended for use in all phases of the creation of electronic systems. Because it is both machine readable and human readable, it supports the development, verification, synthesis, and testing of hardware designs; the communication of hardware design data; and the maintenance, modification, and procurement of hardware. Its primary audiences are the implementors of tools supporting the language and the advanced users of the language. Keywords: computer languages, electronic systems, hardware, hardware design, VHDL.
