1. Record Nr. UNISALENTO991000502529707536

Autore	Stella Richter, Paolo
Titolo	I titoli abilitativi in edilizia : commento al T. U. in materia di edilizia in vigore dal 30 giugno 2003 / Paolo Stella Richter
Pubbl/distr/stampa	Torino : UTET, c2003
ISBN	8802060665
Descrizione fisica	viii, 179 p. ; 24 cm
Collana	Il diritto attuale ; 45
Disciplina	346.45045
Soggetti	Edilizia - Diritto Denuncia di inizio attività Concessione edilizia
Lingua di pubblicazione	Italiano
Formato	Materiale a stampa
Livello bibliografico	Monografia

2. Record Nr. UNINA9910137847103321

Autore	Polona Dobnik Dubrovski
Titolo	Woven fabric engineering / / edited by Polona Dobnik Dubrovski
Pubbl/distr/stampa	IntechOpen, 2010 Rijeka, Croatia : , : IntechOpen, , [2010] ©2010
ISBN	953-51-4541-X
Edizione	[1st ed.]
Descrizione fisica	1 online resource (436 pages) : illustrations
Disciplina	677.6
Soggetti	Industrial fabrics
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references.
Sommario/riassunto	The main goal in preparing this book was to publish contemporary concepts, new discoveries and innovative ideas in the field of woven fabric engineering, predominantly for the technical applications, as well as in the field of production engineering and to stress some problems connected with the use of woven fabrics in composites. The advantage of the book Woven Fabric Engineering is its open access fully searchable by anyone anywhere, and in this way it provides the forum for dissemination and exchange of the latest scientific information on theoretical as well as applied areas of knowledge in the field of woven fabric engineering. It is strongly recommended for all those who are connected with woven fabrics, for industrial engineers, researchers and graduate students.

3.	Record Nr.	UNINA9910746982603321
	Autore	Reinders James
	Titolo	Data Parallel C++ : Programming Accelerated Systems Using C++ and SYCL / / by James Reinders, Ben Ashbaugh, James Brodman, Michael Kinsner, John Pennycook, Xinmin Tian
	Pubbl/distr/stampa	2023 Berkeley, CA : , : Apress : , : Imprint : Apress, , 2023
	ISBN	9781484296912 1484296915
	Edizione	[2nd ed. 2023.]
	Descrizione fisica	1 online resource (XXX, 630 p. 329 illus., 294 illus. in color.)
	Classificazione	COM051010COM067000
	Disciplina	005.45
	Soggetti	Compilers (Computer programs) Makerspaces Compilers and Interpreters Maker
	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
	Nota di contenuto	Chapter 1: Introduction -- Chapter 2: Where Code Executes -- Chapter 3: Data Management and Ordering the Uses of Data -- Chapter 4: Expressing Parallelism -- Chapter 5: Error Handling -- Chapter 6: Unified Shared Memory -- Chapter 7: Buffers -- Chapter 8: Scheduling Kernels and Data Movement -- Chapter 9: Local Memory and Work-group Barriers -- Chapter 10: Defining Kernels -- Chapter 11: Vector and Math Arrays -- Chapter 12: Device Information and Kernel Specialization -- Chapter 13: Practical Tips -- Chapter 14: Common Parallel Patterns -- Chapter 15: Programming for GPUs -- Chapter 16: Programming for CPUs -- Chapter 17: Programming for FFGAs -- Chapter 18: Libraries -- Chapter 19: Memory Model and Atomics -- Chapter 20: Backend Interoperability -- Chapter 21: Migrating CUDA Code -- Epilogue.
	Sommario/riassunto	"This book, now in is second edition, is the premier resource to learn SYCL 2020 and is the ONLY book you need to become part of this community." Erik Lindahl, GROMACS and Stockholm University Learn how to accelerate C++ programs using data parallelism and SYCL. This

open access book enables C++ programmers to be at the forefront of this exciting and important development that is helping to push computing to new levels. This updated second edition is full of practical advice, detailed explanations, and code examples to illustrate key topics. SYCL enables access to parallel resources in modern accelerated heterogeneous systems. Now, a single C++ application can use any combination of devices–including GPUs, CPUs, FPGAs, and ASICs–that are suitable to the problems at hand. This book teaches data-parallel programming using C++ with SYCL and walks through everything needed to program accelerated systems. The book begins by introducing data parallelism and foundational topics for effective use of SYCL. Later chapters cover advanced topics, including error handling, hardware-specific programming, communication and synchronization, and memory model considerations. All source code for the examples used in this book is freely available on GitHub. The examples are written in modern SYCL and are regularly updated to ensure compatibility with multiple compilers. You Will Learn How to: Accelerate C++ programs using data-parallel programming Use SYCL and C++ compilers that support SYCL Write portable code for accelerators that is vendor and device agnostic Optimize code to improve performance for specific accelerators Be poised to benefit as new accelerators appear from many vendors.