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	Nota di contenuto	-- A Logic-Based Physical Simulation Framework for Digital Microfluidic Biochips. -- A Parallel Synchronous Execution Engine and Target Language for Digital Microfluidics. -- QCEDA: Using Quantum Computers for EDA. -- Real-Time Linux on RISC-V: Long-Term Performance Analysis of PRE EMPT RT Patches. -- RV-VP2: Unlocking the Potential of RISC-V Packed-SIMD for Embedded Processing. -- A Novel System Simulation Framework for HBM2 FPGA Platforms. -- ONNX-to-Hardware Design Flow for Adaptive Neural-Network Inference on FPGAs. -- Efficient Post-Training Augmentation for Adaptive Inference in Heteroge neous and Distributed IoT Environments. -- Pooling On-the-go for NoC-based Convolutional

Neural Network Accelerator. -- Vitamin-V: Serverless Cloud Computing Porting on RISC-V. -- Design and Implementation of an Open Source OpenGL SC 2.0.1 Installable Client Driver and Offline Compiler. -- Plan your defense: A comparative analysis of leakage detection methods on RISC-V cores. -- iVault: Leveraging Architectural Code Concealing Techniques to Protect Cryptographic Keys. -- I2DS: FPGA-based Deep Learning Industrial Intrusion Detection System. -- ACRA: A Cutting-Edge Analytics Platform for Advanced Real-Time Corruption Risk Assessment and Investigation Prioritization. -- Post Quantum Cryptography Research Lines in the Italian Center for Security and Rights in Cyberspace. -- Advancing Future 5G/B5G Systems: The Int5Gent Approach. -- RISC-V Accelerators, Enablement and Applications for Automotive and Smart Home in the ISOLDE Project. -- PMDI: An AI-enabled Ecosystem for Cooperative Urban Mobility. -- Open Source Software Randomisation Framework for Probabilistic WCET Prediction on Multicore CPUs, GPUs and Accelerators. -- A Hypervisor Based Platform for the Development and Verification of Reliable Software Applications.

Sommario/riassunto	The two-volume set LNCS 15226 and 15227 constitutes the refereed proceedings of the 24th International Conference on Embedded Computer Systems: Architectures, Modeling, and Simulation, SAMOS 2024, held in Samos, Greece, during June 29–July 4, 2024. The 24 full papers, 10 invited full papers and 4 poster papers included in the proceedings were carefully reviewed and selected from 57 submissions. They deal with embedded computer systems focusing on their applications; architectures; and underlying processors, as well as methods created to automate their design.