1. Record Nr. UNISA996575086803316 1685-2022: IEEE Standard for IP-XACT, Standard Structure for **Titolo** Packaging, Integrating, and Reusing IP within Tool Flows / / Institute of **Electrical and Electronics Engineers** New York:,: IEEE,, [2023] Pubbl/distr/stampa **ISBN** 1-5044-9448-2 Descrizione fisica 1 online resource (750 pages) Disciplina 620.0042 Soggetti Engineering design Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Sommario/riassunto Conformance checks for eXtensible Markup Language (XML) data designed to describe electronic systems are formulated by this standard. The meta-data forms that are standardized include components, systems, bus interfaces and connections, abstractions of those buses, and details of the components including address maps, register and field descriptions, and file set descriptions for use in automating design, verification, documentation, and use flows for electronic systems. A set of XML schemas of the form described by the World Wide Web Consortium (W3C(R)) and a set of semantic consistency rules (SCRs) are included. A generator interface that is portable across tool environments is provided. The specified combination of methodology-independent meta-data and the tool-independent mechanism for accessing that data provides for portability of design

data, design methodologies, and environment implementations.