Record Nr. UNISA996574841503316 **Titolo** 1481-2019 - IEEE Standard for Integrated Circuit (IC) Open Library Architecture (OLA) / / Institute of Electrical and Electronics Engineers Pubbl/distr/stampa New York, New York:,: IEEE,, 2020 **ISBN** 1-5044-6355-2 1 online resource (641 pages) Descrizione fisica Disciplina 621.381531 Soggetti Printed circuits - Design and construction Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Ways for integrated circuit designers to analyze chip timing and power Sommario/riassunto consistently across a broad set of electric design automation (EDA) applications are covered in this standard. Methods by which integrated circuit vendors can express timing and power information once per given technology are also covered. In addition, the means by which EDA vendors can meet their application performance and capacity needs are discussed.