

1. Record Nr.	UNINA9910452233703321
Autore	Swarts Heidi J
Titolo	Organizing urban America [[electronic resource]] : secular and faith-based progressive movements / / Heidi J. Swarts
Pubbl/distr/stampa	Minneapolis, : University of Minnesota Press, 2008
ISBN	0-8166-5384-4
Descrizione fisica	1 online resource (333 p.)
Collana	Social movements, protest, and contention ; ; v. 28
Disciplina	307.3/4160973
Soggetti	Community organization - United States Community development, Urban - United States Urban renewal - United States - Citizen participation Religion and politics - United States Electronic books.
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Introduction. Invisible actors : community organizing, agenda setting, and American social movements -- Different mobilizing cultures : congregation-based organizing and ACORN -- Religion and progressive politics : congregation-based Community Organizing's innovative cultural strategy -- Experimenting with national organizing campaigns : ACORN's innovative political strategy -- Organizing is a numbers game : St. Louis ACORN -- A seat at the regional table : Metropolitan Congregations United for St. Louis -- La puebla unida : ACORN in the Sunbelt -- The power is in the relationship : San Jose PACT -- The results of organizing -- American inequality and the potential of community organizing.
Sommario/riassunto	Heidi J. Swarts explores activist groupsO cultural, organizational, and political strategies. Focusing on ACORN chapters and church federations, Swarts demonstrates how congregation-based organizing has developed an innovative cultural strategy, and how ACORNs national structure allows it to coordinate campaigns quickly. By making these often-invisible grassroots organizers evident, Swarts sheds light on factors that constrain or enable other social movements in the United States.

2. Record Nr.	UNISA996472070103316
Titolo	Applications of evolutionary computation : 25th European conference, EvoApplications 2022, held as part of EvoStar 2022, Madrid, Spain, April 20-22, 2022, proceedings / / edited by Juan Luis Jimenez Laredo, J. Ignacio Hidalgo, and Kehinde Oluwatoyin Babaagba
Pubbl/distr/stampa	Cham, Switzerland : , : Springer, , [2022] ©2022
ISBN	3-031-02462-1
Descrizione fisica	1 online resource (759 pages)
Collana	Lecture Notes in Computer Science ; ; v.13224
Disciplina	006.3823
Soggetti	Evolutionary computation
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Intro -- Preface -- Organization -- Contents -- Applications of Evolutionary Computation -- An Enhanced Opposition-Based Evolutionary Feature Selection Approach -- 1 Introduction -- 2 Moth Flame Optimization -- 2.1 Binary Moth Flame Optimization -- 2.2 Binary Moth Flame Optimization for Feature Selection -- 3 The Proposed Approach -- 3.1 Initialization Using Opposition-Based Method -- 3.2 Retiring Flame -- 4 Experimental Setup and Results -- 5 Conclusions -- References -- A Methodology for Determining Ion Channels from Membrane Potential Neuronal Recordings -- 1 Introduction -- 2 Conductance-Based Model Description -- 3 Defining a Benchmark with Known Types of Ion Channels -- 4 Methodology and Experimental Setup -- 5 Experimental Results -- 6 Conclusions -- A Mathematical Description of the Models -- B Experimental Setup and Parameter Ranges -- References -- Swarm Optimised Few-View Binary Tomography -- 1 Introduction -- 2 Binary Tomographic Reconstruction -- 3 Swarm Optimisation -- 4 Constrained Search in High Dimensions -- 5 Reconstructions -- 6 Results -- 7 Discussion -- 8 Conclusions -- References -- Comparing Basin Hopping with Differential Evolution and Particle Swarm Optimization -- 1 Introduction -- 2 The Metaheuristics Studied -- 2.1 Basin Hopping -- 2.2 Differential Evolution -- 2.3 Particle Swarm Optimization -- 3 The Benchmarking Environment -- 4

Experimental Setup -- 5 Experimental Results -- 6 Conclusions --
References -- Combining the Properties of Random Forest with
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Random Structured Grammatical Evolution for Symbolic Regression
Problems -- 3 Experimental Setup -- 3.1 Study Problems -- 3.2
Configuration of the Algorithms -- 4 Results -- 5 Conclusions --
References.

EvoCC: An Open-Source Classification-Based Nature-Inspired
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Related Works -- 3 Methodology -- 4 Framework Overview -- 4.1
Parameters -- 4.2 Datasets -- 4.3 Clustering with EvoCluster -- 4.4
Classification -- 4.5 Evaluation Measures -- 4.6 Results Management
-- 5 Experiments and Visualizations -- 6 Conclusion and Future Works
-- References -- Evolution of Acoustic Logic Gates in Granular
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Simulation Setup -- 3.1 2D Granular Simulator -- 3.2 Optimization
Method -- 4 Results and Discussion -- 4.1 Evolution of an Acoustic
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5 Conclusion and Future Work -- References -- Public-Private
Partnership: Evolutionary Algorithms as a Solution to Information
Asymmetry -- 1 Introduction -- 2 The Problem -- 3 Proposed
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-- 4 Experimental Evaluation -- 4.1 Stochastic Optimization -- 4.2
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References -- The Asteroid Routing Problem: A Benchmark for
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2.3 Lambert Problem -- 3 Asteroid Routing Problem -- 4 Optimization
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4.2 Greedy Nearest Neighbor Heuristic -- 4.3 Unbalanced Mallows
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-- 6 Conclusions -- References -- On the Difficulty of Evolving
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3 Incremental Construction with EA -- 3.1 Evolving Subsets of
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5 Conclusions and Future Work -- References -- Improving the
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Experiments over 50 Dimensions -- 5 Conclusions and Future Work --
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-- 2 Background -- 3 Methods -- 3.1 Individual Encoding -- 3.2
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Dataset -- 4.2 Back-Propagation Neural Networks (BPNNs) -- 4.3 Computational Setup and Data Preparation -- 5 Experimental Results -- 6 Conclusions -- References -- Explainable Landscape Analysis in Automated Algorithm Performance Prediction -- 1 Introduction -- 2 Related Work -- 3 Automated Algorithm Performance Prediction -- 4 Experimental Setup -- 4.1 Data -- 4.2 Regression Models and Their Hyper-parameters -- 4.3 Evaluation -- 5 Results and Discussion -- 6 Conclusion -- References.

Search Trajectories Networks of Multiobjective Evolutionary Algorithms -- 1 Introduction -- 2 Related Work -- 3 Preliminaries -- 3.1 Search Trajectory Networks -- 3.2 Multiobjective Optimisation Problems -- 4 STN Extension for the Multiobjective Domain -- 5 Experiments -- 5.1 Experimental Parameters -- 5.2 Metrics -- 5.3 Reproducibility -- 6 Results -- 7 Conclusion -- References -- EvoMCS: Optimising Energy and Throughput of Mission Critical Services -- 1 Introduction -- 2 Related Work -- 3 EvoMCS: Multi-objective Optimization -- 3.1 Scenario and Technologies -- 3.2 Evolutionary Algorithm -- 3.3 Heuristic for Fitness -- 3.4 Selection Strategy -- 3.5 Operators to Generate Descendants -- 4 Experimentation -- 4.1 Validation Scenarios -- 4.2 Configuration Parameters -- 4.3 Evaluation Metrics -- 4.4 Profiles Validation - Inputs from EvoMCS -- 5 Results -- 5.1 Operators for the EvoMCS in H1(E/T) -- 5.2 Optimal Configurations -- 5.3 Optimal Profiles in Scenarios with Dense-Environments -- 6 Conclusions -- References -- RWS-L-SHADE: An Effective L-SHADE Algorithm Incorporation Roulette Wheel Selection Strategy for Numerical Optimisation -- 1 Introduction -- 2 Background -- 2.1 Differential Evolution -- 2.2 L-SHADE -- 3 RWS-L-SHADE -- 4 Experimental Results -- 5 Conclusions -- References -- WebGE: An Open-Source Tool for Symbolic Regression Using Grammatical Evolution -- 1 Introduction -- 2 Grammatical Evolution and Differential Evolution -- 3 Software Description -- 3.1 Modular Design -- 3.2 Parallel Execution -- 3.3 Persistence Layer -- 3.4 Implementation Technologies -- 4 WebGE Most Relevant Features -- 4.1 GUI for Experiments Management -- 4.2 Cross-fold Validation -- 4.3 Detailed Statistics -- 5 Use Case: Vladislavleva-4 -- 6 Conclusions -- References -- A New Genetic Algorithm for Automated Spectral Pre-processing in Nutrient Assessment.

1 Introduction -- 1.1 Goals -- 1.2 Organisation -- 2 Background and Related Work -- 2.1 Vibrational Spectroscopy -- 2.2 Partial Least Squares Regression -- 2.3 Spectral Pre-processing -- 2.4 PLSR for Nutrient Assessment -- 3 The Proposed Approach -- 3.1 Representations for the Two Populations for Co-evolution -- 3.2 Mapping of the Two Populations for Pairwise Evaluations -- 3.3 The Evaluation Method -- 4 Experiment Design -- 4.1 Datasets -- 4.2 Parameter Settings -- 5 Results and Discussions -- 5.1 Comparisons on the Training and Test Performance -- 5.2 Analyses on the Pre-processing Selection -- 5.3 Analyses on Feature Selection Results -- 6 Conclusions and Future Work -- References -- Evolutionary Computation in Edge, Fog, and Cloud Computing -- Dynamic Hierarchical Structure Optimisation for Cloud Computing Job Scheduling -- 1 Introduction -- 2 Related Work -- 3 Job Scheduling Structures -- 4 Structure Optimisation -- 4.1 Brute Force Search Algorithm -- 4.2 Genetic Algorithm -- 4.3 Simulated Annealing Algorithm -- 5 Simulation Experiments and Results -- 5.1 Setup -- 5.2 Experiment 1: Search Algorithm Comparison -- 5.3 Experiment 2: Server Processing Power Dispersion Impact -- 5.4 Experiment 3: Task Size Dispersion Impact -- 5.5 Experiment 4: Job Complexity Impact -- 6 Conclusion -- References -- Optimising Communication Overhead in

Federated Learning Using NSGA-II -- 1 Introduction -- 2 Fundamental Concepts -- 2.1 Federated Learning -- 2.2 Communication Overhead in Distributed Deep Learning -- 3 Proposed Approach -- 3.1 The Proposed FL-COP Modelling and Formulation -- 3.2 The Communication-Overhead Reduction Routine -- 4 Experimental Study and Analysis -- 4.1 Problem Benchmarks and Experimental Settings -- 4.2 Experimental Results and Discussion -- 5 Conclusions and Perspectives -- References -- Evolutionary Machine Learning. Evolving Data Augmentation Strategies.

3. Record Nr.	UNINA9910484163603321
Titolo	Integrated Circuit and System Design: Power and Timing Modeling, Optimization and Simulation : 19th International Workshop, PATMOS 2009, Delft, The Netherlands, September 9-11, 2009, Revised Selected Papers // edited by José Monteiro, Rene van Leuken
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2010
ISBN	1-280-38568-5 9786613563606 3-642-11802-X
Edizione	[1st ed. 2010.]
Descrizione fisica	1 online resource (370 p. 234 illus.)
Collana	Theoretical Computer Science and General Issues, , 2512-2029 ; ; 5953
Classificazione	SS 4800
Altri autori (Persone)	MonteiroJose <1966-> Van LeukenRene
Disciplina	621.39/5
Soggetti	Computer systems Computer programming Computer engineering Computer networks Microprocessors Computer architecture Computer simulation Computers Computer System Implementation Programming Techniques Computer Engineering and Networks Processor Architectures Computer Modelling Computer Hardware

Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	<p>Keynotes -- Robust Low Power Embedded SRAM Design: From System to Memory Cell -- Variability in Advanced Nanometer Technologies: Challenges and Solutions -- Subthreshold Circuit Design for Ultra-Low-Power Applications -- Special Session -- SystemC AMS Extensions: New Language – New Methods – New Applications -- Session 1: Variability & Statistical Timing -- Process Variation Aware Performance Analysis of Asynchronous Circuits Considering Spatial Correlation -- Interpreting SSTA Results with Correlation -- Residue Arithmetic for Variation-Tolerant Design of Multiply-Add Units -- Exponent Monte Carlo for Quick Statistical Circuit Simulation -- Poster Session 1: Circuit Level Techniques -- Clock Repeater Characterization for Jitter-Aware Clock Tree Synthesis -- A Hardware Implementation of the User-Centric Display Energy Management -- On-chip Thermal Modeling Based on SPICE Simulation -- Switching Noise Optimization in the Wake-Up Phase of Leakage-Aware Power Gating Structures -- Session 2: Power Management -- Application-Specific Temperature Reduction Systematic Methodology for 2D and 3D Networks-on-Chip -- Data-Driven Clock Gating for Digital Filters -- Power Management and Its Impact on Power Supply Noise -- Assertive Dynamic Power Management (AsDPM) Strategy for Globally Scheduled RT Multiprocessor Systems -- Session 3: Low Power Circuits & Technology -- Design Optimization of Low-Power 90nm CMOS SOC Application Using 0.5V Bulk PMOS Dynamic-Threshold with Dual Threshold (MTCMOS): BP-DTMOS-DT Technique -- Crosstalk in High-Performance Asynchronous Designs -- Modeling and Reducing EMI in GALS and Synchronous Systems -- Low-Power Dual-Edge Triggered State Retention Scan Flip-Flop -- Poster Session 2: System Level Techniques -- Multi-granularity NoC Simulation Framework for Early Phase Exploration of SDR Hardware Platforms -- Dynamic Data Type Optimization and Memory Assignment Methodologies -- Accelerating Embedded Software Power Profiling Using Run-Time Power Emulation -- Write Invalidation Analysis in Chip Multiprocessors -- Practical Design Space Exploration of an H264 Decoder for Handheld Devices Using a Virtual Platform -- BSAA: A Switching Activity Analysis and Visualisation Tool for SoC Power Optimisation -- Session 4: Power & Timing Optimization Techniques -- Reducing Timing Overhead in Simultaneously Clock-Gated and Power-Gated Designs by Placement-Aware Clustering -- Low Energy Voltage Dithering in Dual V DD Circuits -- Product On-Chip Process Compensation for Low Power and Yield Enhancement -- Session 5: Self-timed Circuits -- Low-Power Soft Error Hardened Latch -- Digital Timing Slack Monitors and Their Specific Insertion Flow for Adaptive Compensation of Variabilities -- Quasi-Delay-Insensitive Computing Device: Methodological Aspects and Practical Implementation -- The Magic Rule of Tiles: Virtual Delay Insensitivity -- Session 6: Low Power Circuit Analysis & Optimization -- Analysis of Power Consumption Using a New Methodology for the Capacitance Modeling of Complex Logic Gates -- A New Methodology for Power-Aware Transistor Sizing: Free Power Recovery (FPR) -- Routing Resistance Influence in Loading Effect on Leakage Analysis -- Session 7: Low Power Design Studies -- Processor Customization for Software Implementation of the AES Algorithm for Wireless Sensor</p>

Networks -- An On-Chip Multi-mode Buck DC-DC Converter for Fine-Grain DVS on a Multi-power Domain SoC Using a 65-nm Standard CMOS Logic Process -- Energy Dissipation Reduction of a Cardiac Event Detector in the Sub-V_t Domain By Architectural Folding -- A New Optimized High-Speed Low-Power Data-Driven Dynamic (D3L) 32-Bit Kogge-Stone Adder.

Sommario/riassunto

This book constitutes the thoroughly refereed post-conference proceedings of 19th International Workshop on Power and Timing Modeling, Optimization and Simulation, PATMOS 2009, featuring Integrated Circuit and System Design, held in Delft, The Netherlands during September 9-11, 2009. The 26 revised full papers and 10 revised poster papers presented were carefully reviewed and selected from numerous submissions. The papers are organized in topical sections on variability & statistical timing, circuit level techniques, power management, low power circuits & technology, system level techniques, power & timing optimization techniques, self-timed circuits, low power circuit analysis & optimization, and low power design studies. .
