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Titolo	Job Scheduling Strategies for Parallel Processing [[electronic resource]] : 19th and 20th International Workshops, JSSPP 2015, Hyderabad, India, May 26, 2015 and JSSPP 2016, Chicago, IL, USA, May 27, 2016, Revised Selected Papers // edited by Narayan Desai, Walfredo Cirne
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Descrizione fisica	1 online resource (X, 279 p. 114 illus.)
Collana	Theoretical Computer Science and General Issues, , 2512-2029 ; ; 10353
Disciplina	004.24
Soggetti	Software engineering Computer networks Algorithms Application software Computer simulation Software Engineering Computer Communication Networks Computer and Information Systems Applications Computer Modelling
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Parallel scheduling challenges raising at multiple levels of abstractions -- node level parallelism -- Minimization of energy consumption in task migration within a many-core chip; -- Task replication in real-time scheduling context -- Data-driven approach to schedule GPU load -- The use of lock-free data structures in OS scheduler -- The influence between user behaviour (think time, more precisely) and parallel scheduling -- Evalix, a predictor for job resource consumption -- Sophisticated and realistic simulation -- Space-filling curves leading to better scheduling of large-scale computers -- Discussion of real-life production experiences.
Sommario/riassunto	This book constitutes the thoroughly refereed post-conference

proceedings of the 19th and 20th International Workshop on Job Scheduling Strategies for Parallel Processing, JSSPP 2015 and 2016, held respectively in Hyderabad, India, on May 26, 2015 and in Chicago, IL, USA, on May 27, 2016. The 14 revised full papers presented (7 papers in 2015 and 7 papers in 2016) were carefully reviewed and selected from 28 submissions (14 in 2015 and 14 in 2016). The papers cover the following topics: parallel scheduling raising challenges multiple levels of abstractions; node level parallelism; minimization of energy consumption in task migration within a many-core chip; task replication in real-time scheduling context; data-driven approach to schedule GPU load; the use of lock-free data structures in OS scheduler; the influence between user behaviour (think time, more precisely) and parallel scheduling; Evalix, a predictor for job resource consumption; sophisticated and realistic simulation; space-filling curves leading to better scheduling of large-scale computers; discussion of real-life production experiences.
