Record Nr. UNISA996466459003316 Runtime Verification [[electronic resource]]: 6th International **Titolo** Conference, RV 2015, Vienna, Austria, September 22-25, 2015. Proceedings / / edited by Ezio Bartocci, Rupak Majumdar Cham:,: Springer International Publishing:,: Imprint: Springer,, Pubbl/distr/stampa 2015 **ISBN** 3-319-23820-5 Edizione [1st ed. 2015.] 1 online resource (XIX, 432 p. 133 illus.) Descrizione fisica Programming and Software Engineering;; 9333 Collana 001.642 Disciplina Soggetti Software engineering Algorithms Computer logic Computer programming Programming languages (Electronic computers) Mathematical logic Software Engineering Algorithm Analysis and Problem Complexity Logics and Meanings of Programs **Programming Techniques** Programming Languages, Compilers, Interpreters Mathematical Logic and Formal Languages Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Note generali Bibliographic Level Mode of Issuance: Monograph Invited Papers -- Towards a Verified Artificial Pancreas: Challenges and Nota di contenuto Solutions for Runtime Verification -- Twenty Years of Dynamic Software Model Checking -- Explaining Heisenbugs -- Regular Papers --Qualitative and Quantitative Monitoring of Spatio-Temporal Properties -- Runtime Adaptation for Actor Systems -- Robust Online Monitoring of Signal Temporal Logic -- On Verifying The Hennessy-Milner Logic

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Sommario/riassunto

This book constitutes the refereed proceedings of the 6th International Conference on Runtime Verification, RV 2015, held in Vienna, Austria in September 2015. The 15 revised full papers presented together with 4 short papers, 2 tool papers, 4 tutorials, 3 invited talks, and 2 software competition papers were carefully reviewed and selected from 45 submissions. The discussion of the conference centers around two main aspects. The first is to understand whether the runtime verification techniques can practically complement the traditional methods proving programs correct before their execution, such as model checking and theorem proving. The second concerns with formal methods and how their application can improve traditional ad-hoc monitoring techniques used in performance monitoring, hardware design emulation and simulation, etc.