

1. Record Nr.	UNISA996465857503316
Titolo	Field-Programmable Logic, Smart Applications, New Paradigms and Compilers [[electronic resource]] : 6th International Workshop on Field-Programmable Logic and Applications, FPL '96, Darmstadt, Germany, September 23 - 25, Proceedings / / edited by Reiner W. Hartenstein, Manfred Glesner
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 1996
ISBN	3-540-70670-4
Edizione	[1st ed. 1996.]
Descrizione fisica	1 online resource (X, 436 p.)
Collana	Lecture Notes in Computer Science, , 0302-9743 ; ; 1142
Disciplina	621.39/5
Soggetti	Architecture, Computer Logic design Microprocessors Computational complexity Computer-aided engineering Electronics Microelectronics Computer System Implementation Logic Design Register-Transfer-Level Implementation Complexity Computer-Aided Engineering (CAD, CAE) and Design Electronics and Microelectronics, Instrumentation
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di contenuto	Portable pipeline synthesis for FCCMs -- Performance-directed technology mapping for LUT-based FPGAs — What role do decomposition and covering play? -- A framework for developing parametrised FPGA libraries -- FACT: Co-evaluation environment for FPGA architecture and CAD system -- An universal CLA adder generator for SRAM-based FPGAs -- An emulation system of the

WASMII: A data driven computer on a virtual hardware -- Costum computing machines vs. Hardware/Software Co-Design: From a globalized point of view -- The design of a coprocessor board using Xilinx's XC6200 FPGA — An experience report -- RACE: Reconfigurable and adaptive computing environment -- Computing 2-D DFTs using FPGAs -- CAPpartx: Computer aided prototyping partitioning for Xilinx FPGAs, a hierarchical partitioning tool for rapid prototyping -- Architectural synthesis and efficient circuit implementation for field programmable gate arrays -- RaPiD — Reconfigurable pipelined datapath -- Solving satisfiability problems on FPGAs -- FPGA implementation of the block-matching algorithm for motion estimation in image coding -- Parallel CRC computation in FPGAs -- Coherent demodulation with FPGAs -- The Trianus system and its application to custom computing -- Logic synthesis for FPGAs using a mixed exclusive-/inclusive-OR form -- Flexible codesign target architecture for early prototyping of CMIST systems -- Attempt-1: A reconfigurable multiprocessor testbed -- A slow motion engine for the analysis of FPGA-based prototypes -- Implementing reconfigurable datapaths in FPGAs for adaptive filter design -- A fast constant coefficient multiplier for the XC6200 -- Key issues for user acceptance of FPGA design tools -- Reconfigurable DSP demonstrators for the development of spacecraft payload processors -- Reconfigurable logic based fibre channel network card with sub 2 ?s raw latency -- An asynchronous transfer mode (ATM) stream demultiplexer and switch -- Optically reconfigurable FPGAs: Is this a future trend? -- CCSimP — An instruction-level costum-configurable processor for CPLDs -- Architectural synthesis techniques for dynamically reconfigurable logic -- Fast reconfigurable crossbar switching in FPGAs -- Growable FPGA macro generator -- Architectural strategies for implementing an image processing algorithm on XC6000 FPGA -- A virtual hardware operating system for the Xilinx XC6200 -- An experimental programmable environment for prototyping digital circuits -- Migration from schematic-based designs to a VHDL synthesis environment -- ASIC design and FPGA design: A unified design methodology applied to different technologies -- FIR filtering with FPGAs using quadrature sigma-delta modulation encoding -- A new FPGA technology mapping approach by cluster merging -- An EPLD based transient recorder for simulation of video signal processing devices in a VHDL environment close to system level conditions -- Convolutional error decoding with FPGAs -- Metastability characteristics testing for programmable logic design -- Implementing ?? modulator prototype designs on an FPGA -- Design of a VME parameterized library for FPGAs -- Development of a telephone answering machine in a lab — FPGAs in Education -- FPGA design migration: Some remarks -- Concurrent design of hardware/software dedicated systems -- The implementation of a field programmable logic based co-processor for the acceleration of discrete event simulators -- Computing weight distributions of binary linear block codes on a CCM.

Sommario/riassunto

This book constitutes the refereed proceedings of the 6th International Workshop of Field-Programmable Logic and Applications, FPL '96, held in Darmstadt, Germany, in September 1996. The 37 revised full papers presented in the book are selected from 82 submissions originating from 27 countries; also included are 13 high-quality poster presentations. The book is divided into topical sections on high-level design, new software and hardware development tools, custom computers, applications, hardware/software co-design, AISC emulators, vendor session, industrial applications and experiences, reconfiguration aspects, CAD user experiences, and miscellaneous.

2. Record Nr.	UNISA996466300103316
Titolo	KI 2019: Advances in Artificial Intelligence [[electronic resource]] : 42nd German Conference on AI, Kassel, Germany, September 23–26, 2019, Proceedings / / edited by Christoph Benzmüller, Heiner Stuckenschmidt
Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2019
ISBN	3-030-30179-6
Edizione	[1st ed. 2019.]
Descrizione fisica	1 online resource (XI, 354 p. 170 illus., 60 illus. in color.)
Collana	Lecture Notes in Artificial Intelligence ; ; 11793
Disciplina	006.3
Soggetti	Artificial intelligence Mathematical logic Algorithms Computers Computer organization Artificial Intelligence Mathematical Logic and Formal Languages Algorithm Analysis and Problem Complexity Information Systems and Communication Service Computer Systems Organization and Communication Networks
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Towards Computational Persuasion via Natural Language Argumentation Dialogues -- Analogy-Based Preference Learning with Kernels -- Data Acquisition for Argument Search: The args.me Corpus -- Monotone and Online Fair Division -- A human-oriented System for Equational Reasoning -- Mixing Description Logics in Privacy-Preserving Ontology Publishing -- Clustering of Argument Graphs Using Semantic Similarity Measures -- Reducing Search Space of Genetic Algorithms for Fast Black Box Attacks on Image Classifiers -- An Empirical Study of the Usefulness of State-Dependent Action Costs in Planning -- Strong Equivalence for Argumentation Frameworks with Collective Attacks -- Developing Fuzzy Inference Systems from

Qualitative Interviews for Travel Mode Choice in an Agent-based Mobility Simulation -- Monte-Carlo Search for Prize-Collecting Robot Motion Planning with Time Windows, Capacities, Pickups, and Deliveries -- Automated Robot Skill Learning from Demonstration for Various Robot Systems -- Learning Gradient-based ICA by Neurally Estimating Mutual Information -- Enhancing Explainability of Deep Reinforcement Learning Through Selective Layer-Wise Relevance Propagation.-A Crow Search-based Genetic Algorithm for Solving Two-dimensional Bin Packing Problem -- Extracting Reasons for Moral Judgments Under Various Ethical Principles -- Gaussian Lifted Marginal Filtering -- An Introduction to AnyBURL -- Simplifying Automated Pattern Selection for Planning with Symbolic Pattern Databases -- ALICA 2.0 - Domain-Independent Teamwork -- Extending Modular Semantics for Bipolar Weighted Argumentation (Extended Abstract) -- Epistemic Multi-Agent Planning Using Monte-Carlo Tree Search -- Towards Intuitive Robot Programming Using Finite State Automata -- Improving Implicit Stance Classification in Tweets Using Word and Sentence Embeddings -- Towards Leveraging Backdoors in Qualitative Constraint Networks -- GAN Path Finder: Preliminary Results -- InformatiCup Competition 2019: Fooling Traffic Sign Recognition -- The Higher-Order Prover Leo-III -- Personalized Transaction Kernels for Recommendation Using MCTS. .

Sommario/riassunto

This book constitutes the refereed proceedings of the 42nd German Conference on Artificial Intelligence, KI 2019, held in Kassel, Germany, in September 2019. The 16 full and 10 short papers presented together with 3 extended abstracts in this volume were carefully reviewed and selected from 82 submissions. KI 2019 has a special focus theme on "AI methods for Argumentation" and especially invited contributions that use methods from all areas of AI to understand, formalize or generate argument structures in natural language.

3. Record Nr.	UNINA9910220155103321
Autore	Wong Carolyn
Titolo	Evaluation of National Institute of Justice-funded geospatial software tools : technical and utility assessments to improve tool development, dissemination, and usage
Pubbl/distr/stampa	RAND Corporation, 2014 [Place of publication not identified], : Rand Corporation, 2014
ISBN	0-8330-8608-1
Descrizione fisica	1 online resource
Disciplina	363.690285
Soggetti	Geospatial data - Management - Evaluation Geospatial data - Collection and preservation - Evaluation Geospatial data - Technological innovations Computer software - Evaluation Geography Earth & Environmental Sciences Geography-General
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Sommario/riassunto	A geospatial software tool-evaluation study conducted for the National Institute of Justice (NIJ) assessed 14 recent NIJ-funded tool developments. The study integrates input from tool developers and users with RAND's independent assessments. The authors found that 12 developments resulted in fully functional tools and make five recommendations that will enable NIJ to maximize benefits to the law enforcement community from future developments.