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   Nota di contenuto   OASIS: An optimizing action-based compiler generator -- Action
                        transformations in the actress compiler generator -- An overview of
                        door attribute grammars -- Coupling evaluators for attribute coupled
                        grammars -- Towards the global optimization of functional logic
                        programs -- A portable and optimizing back end for the SML/NJ
                        compiler -- Efficient organization of control structures in distributed
                        implementations -- Implementing 2DT on a multiprocessor -- Global
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                        for limited connectivity VLIWs -- Delayed exceptions — Speculative
                        execution of trapping instructions -- On the implementation of
                        abstract interpretation systems for (constraint) Logic Programs -- A
                        suite of analysis tools based on a general purpose abstract interpreter
                        -- Flow grammars — a flow analysis methodology -- Provable
                        correctness of prototype interpreters in LDL -- Developing efficient

interpreters based on formal language specifications -- Generating an efficient compiler for a data parallel language from a denotational specification -- Adding semantic and syntactic predicates to LL(k): pred-LL(k) -- Cosy compiler phase embedding with the CoSy compiler model -- Towards provably correct code generation for a hard real-time programming language -- Supporting array dependence testing for an optimizing/parallelizing C compiler -- Processing array statements and procedure interfaces in the PREPARE high performance fortran compiler -- A practical approach to the symbolic debugging of parallelized code -- Reducing the cost of data flow analysis by congruence partitioning -- Interprocedural constant propagation using dependence graphs and a data-flow model -- Solving demand versions of interprocedural analysis problems -- Compile time instruction cache optimizations -- Instruction scheduling over regions: A framework for scheduling across basic blocks -- Transport-triggering vs. operation-triggering.

Sommario/riassunto	The International Conference on Compiler Construction provides a forum for presentation and discussion of recent developments in the area of compiler construction, language implementation and language design. Its scope ranges from compilation methods and tools to implementation techniques for specific requirements on languages and target architectures. It also includes language design and programming environment issues which are related to language translation. There is an emphasis on practical and efficient techniques. This volume contains the papers selected for presentation at CC '94, the fifth International Conference on Compiler Construction, held in Edinburgh, U.K., in April 1994.