

1. Record Nr.	UNISA996466219203316
Titolo	Field-Programmable Logic and Applications: Reconfigurable Computing Is Going Mainstream [[electronic resource] ] : Reconfigurable Computing Is Going Mainstream // edited by Manfred Glesner, Peter Zipf, Michel Renovell
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2002
ISBN	3-540-46117-5
Edizione	[1st ed. 2002.]
Descrizione fisica	1 online resource (XLIV, 1192 p. 330 illus.)
Collana	Lecture Notes in Computer Science, , 0302-9743 ; ; 2438
Disciplina	621.39/5
Soggetti	Architecture, Computer Software engineering Logic design Computer memory systems Microprocessors Computer communication systems Computer System Implementation Software Engineering/Programming and Operating Systems Logic Design Memory Structures Processor Architectures Computer Communication Networks
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Keynote Address -- Trends -- Rapid Prototyping -- FPGA Synthesis -- Custom Computing Engines -- DSP Applications 1 -- Reconfigurable Fabrics -- Dynamic Reconfiguration 1 -- DSP Applications 2 -- Routing & Placement -- Dynamic Reconfiguration 2 -- Power Estimation -- Synthesis Issues -- Keynote Address -- Communication Applications 1 -- New Technologies -- Reconfigurable Architectures -- Communication Applications 2 -- Multimedia Applications -- FPGA-based Arithmetic 1 -- Reconfigurable Processors -- Testing & Fault-

Tolerance -- FPGA-based Arithmetic 2 -- Reconfigurable Systems --  
Image Processing -- Crypto Applications 1 -- Keynote Address --  
Multitasking -- Special Architectures -- Crypto Applications 2 --  
Compilation Techniques -- DSP Applications 3 -- Complex  
Applications -- Architecture Implementation -- Design Flow --  
Miscellaneous -- Short Papers.

---