

1. Record Nr.	UNISA996465920803316
Titolo	Automated Technology for Verification and Analysis [[electronic resource] ] : 5th International Symposium, ATVA 2007 Tokyo, Japan, October 22-25, 2007 Proceedings // edited by Kedar Namjoshi, Tomohiro Yoneda, Teruo Higashino, Yoshio Okamura
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2007
ISBN	3-540-75596-9
Edizione	[1st ed. 2007.]
Descrizione fisica	1 online resource (XIV, 570 p.)
Collana	Programming and Software Engineering ; ; 4762
Disciplina	511.36028563
Soggetti	Computer-aided engineering Computer logic Computers Computer communication systems Special purpose computers Software engineering Computer-Aided Engineering (CAD, CAE) and Design Logics and Meanings of Programs Information Systems and Communication Service Computer Communication Networks Special Purpose and Application-Based Systems Software Engineering
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Invited Talks -- Policies and Proofs for Code Auditing -- Recent Trend in Industry and Expectation to DA Research -- Toward Property-Driven Abstraction for Heap Manipulating Programs -- Branching vs. Linear Time: Semantical Perspective -- Regular Papers -- Mind the Shapes: Abstraction Refinement Via Topology Invariants -- Complete SAT-Based Model Checking for Context-Free Processes -- Bounded Model Checking of Analog and Mixed-Signal Circuits Using an SMT Solver -- Model Checking Contracts – A Case Study -- On the Efficient

Computation of the Minimal Coverability Set for Petri Nets -- Analog/Mixed-Signal Circuit Verification Using Models Generated from Simulation Traces -- Automatic Merge-Point Detection for Sequential Equivalence Checking of System-Level and RTL Descriptions -- Proving Termination of Tree Manipulating Programs -- Symbolic Fault Tree Analysis for Reactive Systems -- Computing Game Values for Crash Games -- Timed Control with Observation Based and Stuttering Invariant Strategies -- Deciding Simulations on Probabilistic Automata -- Mechanizing the Powerset Construction for Restricted Classes of  $\omega$ -Automata -- Verifying Heap-Manipulating Programs in an SMT Framework -- A Generic Constructive Solution for Concurrent Games with Expressive Constraints on Strategies -- Distributed Synthesis for Alternating-Time Logics -- Timeout and Calendar Based Finite State Modeling and Verification of Real-Time Systems -- Efficient Approximate Verification of Promela Models Via Symmetry Markers -- Latticed Simulation Relations and Games -- Providing Evidence of Likely Being on Time: Counterexample Generation for CTMC Model Checking -- Assertion-Based Proof Checking of Chang-Roberts Leader Election in PVS -- Continuous Petri Nets: Expressive Power and Decidability Issues -- Quantifying the Discord: Order Discrepancies in Message Sequence Charts -- A Formal Methodology to Test Complex Heterogeneous Systems -- A New Approach to Bounded Model Checking for Branching Time Logics -- Exact State Set Representations in the Verification of Linear Hybrid Systems with Large Discrete State Space -- A Compositional Semantics for Dynamic Fault Trees in Terms of Interactive Markov Chains -- 3-Valued Circuit SAT for STE with Automatic Refinement -- Bounded Synthesis -- Short Papers -- Formal Modeling and Verification of High-Availability Protocol for Network Security Appliances -- A Brief Introduction to -- On-the-Fly Model Checking of Fair Non-repudiation Protocols -- Model Checking Bounded Prioritized Time Petri Nets -- Using Patterns and Composite Propositions to Automate the Generation of LTL Specifications -- Pruning State Spaces with Extended Beam Search -- Using Counterexample Analysis to Minimize the Number of Predicates for Predicate Abstraction.

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### Sommario/riassunto

This book constitutes the refereed proceedings of the 5th International Symposium on Automated Technology for Verification and Analysis, ATVA 2007, held in Tokyo, Japan, October 22-25, 2007. The 29 revised full papers presented together with 7 short papers were carefully reviewed and selected from 88 submissions. The papers address theoretical methods to achieve correct software or hardware systems, including both functional and non functional aspects; as well as applications of theory in engineering methods and particular domains and handling of practical problems occurring in tools.

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2. Record Nr.	UNINA9910814799403321
Autore	Bybee Rodger W
Titolo	The case for STEM education : challenges and opportunities // Rodger W. Bybee
Pubbl/distr/stampa	Arlington, Virginia : , : NSTA Press, National Science Teachers Association, , [2013] 2013
ISBN	1-938946-92-8
Descrizione fisica	1 online resource (xii, 116 pages) : illustrations
Collana	Gale eBooks
Disciplina	507.1
Soggetti	Science and state Technology and state Science - Study and teaching - Government policy Technology - Study and teaching - Government policy
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references (pages [103]-109) and index.
Nota di contenuto	What are the challenges for STEM education? -- What can we learn from the original Sputnik moment? -- Is STEM education a response to this generation's Sputnik moment? -- How is STEM education different from other education reforms? -- STEM seems to be the answer-what was the question? -- If STEM is an opportunity, what is the federal government's role? -- How can a state, district, or school develop a coherent strategy for STEM education? -- What is your perspective of STEM education? -- STEM education : where are you now, and where do you want to go? -- What is your action plan for STEM education?
Sommario/riassunto	The book starts by putting STEM in context. The early chapters outline the challenges facing STEM education, draw lessons from the Sputnik moment of the 1950s and 1960s, and contrast contemporary STEM with other education reforms. The author then explores appropriate roles for the federal government as well as states, districts, and individual schools. Finally, the book offers several ideas you can use to develop actual action plans for STEM.