

1. Record Nr.	UNIPARTHENOPE000009667
Autore	Bluman, George W.
Titolo	Similarity methods for differential equations / G. W. Bluman, J. D. Cole
Pubbl/distr/stampa	New York; Heidelberg; Berlin : Springer-Verlag, 1974
ISBN	0-387-90107-8 3-540-90107-8
Descrizione fisica	IX, 332 p. ; 26 cm
Collana	Applied mathematical sciences ; 0013
Altri autori (Persone)	Cole, Julian D.
Disciplina	510.8
Collocazione	AMS 510/13
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia

2. Record Nr.	UNISOBSOBE00022194
Autore	Gellius, Aulus
Titolo	I / Aulus Gellius
Pubbl/distr/stampa	London : Heinemann Cambridge, Massachusetts : Harvard University Press, 1970
Descrizione fisica	LXIII, 463 p. ; 16 cm
Collana	<The >Loeb Classical Library ; 195
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Testo latino a fronte
3. Record Nr.	UNISA996465908203316
Titolo	Hardware and Software: Verification and Testing [[electronic resource]] : 6th International Haifa Verification Conference, HVC 2010, Haifa, Israel, October 4-7, 2010. Revised Selected Papers // edited by Sharon Barner, Ian Harris, Daniel Kroening, Orna Raz
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2011
ISBN	3-642-19583-0
Edizione	[1st ed. 2011.]
Descrizione fisica	1 online resource (X, 197 p. 51 illus., 13 illus. in color.)
Collana	Programming and Software Engineering ; ; 6504
Disciplina	005.1
Soggetti	Software engineering Computer logic Programming languages (Electronic computers) Software Engineering Logics and Meanings of Programs Programming Languages, Compilers, Interpreters
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia

Note generali

Bibliographic Level Mode of Issuance: Monograph

Nota di bibliografia

Includes bibliographical references and index.

Sommario/riassunto

This book constitutes the thoroughly refereed post-conference proceedings of the 6th International Haifa Verification Conference, HVC 2010, held in Haifa, Israel in October 2010. The 10 revised full papers presented together with 7 invited papers were carefully reviewed and selected from 30 submissions. The papers address all current issues, challenges and future directions of verification for hardware, software, and hybrid systems and have a research focus on hybrid methods and the migration of methods and ideas between hardware and software, static and dynamic analysis, pre- and post-silicon.