Record Nr. UNISA996465850203316 Euro-Par 2009, Parallel Processing - Workshops [[electronic resource]] **Titolo** : HPPC, HeteroPar, PROPER, ROIA, UNICORE, VHPC, Delft, The Netherlands, August 25-28, 2009, Workshops / / edited by Hai-Xiang Lin, Michael Alexander, Martti Forsell, Andreas Knüpfer, Radu Prodan, Leonel Sousa, Achim Streit Berlin, Heidelberg:,: Springer Berlin Heidelberg:,: Imprint: Springer, Pubbl/distr/stampa . 2010 **ISBN** 1-280-38768-8 9786613565600 3-642-14122-6 Edizione [1st ed. 2010.] 1 online resource (XIX, 468 p. 137 illus.) Descrizione fisica Collana Theoretical Computer Science and General Issues, , 2512-2029 ; ; 6043 004.6 Disciplina Soggetti Computer networks Electronic digital computers—Evaluation Software engineering Algorithms Computer programming Computers, Special purpose Computer Communication Networks System Performance and Evaluation Software Engineering Programming Techniques Special Purpose and Application-Based Systems Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Note generali Bibliographic Level Mode of Issuance: Monograph Nota di bibliografia Includes bibliographical references and index. Third Workshop on Highly Parallel Processing on a Chip (HPPC 2009) --Nota di contenuto HPPC 2009: 3rd Workshop on Highly Parallel Processing on a Chip --The Next 25 Years of Computer Architecture? -- Software Development and Programming of Multi-core SoC -- HPPC 2009 Panel: Are Many-Core Computer Vendors on Track? -- Distance Constrained Mapping to

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