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Titolo	Temporal Logic in Specification [[electronic resource]] : Altrincham, UK, April 8-10, 1987, Proceedings / / edited by Behnam Banieqbal, Howard Barringer, Amir Pnueli
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Descrizione fisica	1 online resource (VIII, 452 p.)
Collana	Lecture Notes in Computer Science, , 0302-9743 ; ; 398
Disciplina	005.13
Soggetti	Programming languages (Electronic computers) Computer logic Mathematical logic Algorithms Software engineering Programming Languages, Compilers, Interpreters Logics and Meanings of Programs Mathematical Logic and Formal Languages Algorithm Analysis and Problem Complexity Software Engineering
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di contenuto	Comparing linear and branching time temporal logics -- Interleaving set temporal logic -- An expressive logic for a process algebra with silent actions -- Temporal logic with fixed points -- On the relation of programs and computations to models of temporal logic -- Specification and verification of concurrent programs by ?-automata -- MCTL — An extension of CTL for modular verification of concurrent systems -- The model checking problem for concurrent systems with many similar processes -- Unified verification theory -- Specifying message passing systems requires extending temporal logic -- Extending interval logic to real time systems -- The prescription and description of state based systems -- Enforcing nondeterminism via linear time temporal logic specifications using hiding -- Specifying

communicating systems with temporal logic -- Abstract object types: A temporal perspective -- Compositionality and modularity in process specification and design: A trace-state based approach -- Using temporal logic for prototyping: The design of a lift controller -- The declarative past and imperative future.
