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| Nota di contenuto       | Technologies and Trends -- Communications Applications -- High Level Design Tools 1 -- Reconfigurable Architectures -- Cryptographic Applications 1 -- Place and Route Tools -- Multi-context FPGAs -- Cryptographic Applications 2 -- Low-Power Issues 1 -- Run-Time Configurations -- Cryptographic Applications 3 -- Compilation Tools -- Asynchronous Techniques -- Biology-Related Applications -- Codesign -- Reconfigurable Fabrics -- Image Processing Applications -- SAT Techniques -- Application-Specific Architectures -- DSP Applications -- Dynamic Reconfiguration -- SoC Architectures -- |

Emulation -- Cache Design -- Arithmetic 1 -- Biologically Inspired Designs -- Low-Power Issues 2 -- SoC Designs -- Cellular Applications -- Arithmetic 2 -- Fault Analysis -- Network Applications -- High Level Design Tools 2 -- Technologies and Trends (Posters) -- Applications (Posters) -- Tools (Posters) -- FPGA Implementations (Posters) -- Video and Image Applications (Posters) -- Reconfigurable and Low-Power Systems (Posters) -- Design Techniques (Posters) -- Neural and Biological Applications (Posters) -- Codesign and Embedded Systems (Posters) -- Reconfigurable Systems and Architectures (Posters) -- DSP Applications (Posters) -- Dynamic Reconfiguration (Posters) -- Arithmetic (Posters) -- Design and Implementations 1 (Posters) -- Design and Implementations 2 (Posters).

#### Sommario/riassunto

This book contains the papers presented at the 13th International Workshop on Field Programmable Logic and Applications (FPL) held on September 1–3, 2003. The conference was hosted by the Institute for Systems and Computer Engineering-Research and Development of Lisbon (INESC-ID) and the Department of Electrical and Computer Engineering of the IST-Technical University of Lisbon, Portugal. The FPL series of conferences was founded in 1991 at Oxford University (UK), and has been held annually since: in Oxford (3 times), Vienna, Prague, Darmstadt, London, Tallinn, Glasgow, Villach, Belfast and Montpellier. It brings together academic researchers, industrial experts, users and newcomers in an informal, welcoming atmosphere that encourages productive exchange of ideas and knowledge between delegates. Exciting advances in field programmable logic show no sign of slowing down. New grounds have been broken in architectures, design techniques, run-time configuration, and applications of field programmable devices in several different areas. Many of these innovations are reported in this volume. The size of FPL conferences has grown significantly over the years. FPL in 2002 saw 214 papers submitted, representing an increase of 83% when compared to the year before. The interest and support for FPL in the programmable logic community continued this year with 216 papers submitted. The technical program was assembled from 90 selected regular papers and 56 posters, resulting in this volume of proceedings. The program also included three invited plenary keynote presentations from LSI Logic, Xilinx and Cadence, and three industrial tutorials from Altera, Mentor Graphics and Dafca.