

1. Record Nr.	UNISA996465617103316
Titolo	Compiler Construction [[electronic resource]] : 9th International Conference, CC 2000 Held as Part of the Joint European Conferences on Theory and Practice of Software, ETAPS 2000 Berlin, Germany, March 25 - April 2, 2000 Proceedings / / edited by David A. Watt
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2000
ISBN	3-540-46423-9
Edizione	[1st ed. 2000.]
Descrizione fisica	1 online resource (XII, 300 p.)
Collana	Lecture Notes in Computer Science, , 0302-9743 ; ; 1781
Disciplina	005.4/53
Soggetti	Programming languages (Electronic computers) Operating systems (Computers) Software engineering Mathematical logic Computer logic Programming Languages, Compilers, Interpreters Operating Systems Software Engineering Mathematical Logic and Formal Languages Logics and Meanings of Programs
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references at the end of each chapters and index.
Nota di contenuto	Shape Analysis -- Optimizing Java Bytecode Using the Soot Framework: Is It Feasible? -- Pipelined Java Virtual Machine Interpreters -- Automatic Removal of Array Memory Leaks in Java -- A Static Study of Java Exceptions Using JESP -- Fast Escape Analysis and Stack Allocation for Object-Based Programs -- Constant Propagation on the Value Graph: Simple Constants and Beyond -- Simple Generation of Static Single-Assignment Form -- Demand-Driven Construction of Call Graphs -- A Framework for Loop Distribution on Limited On-Chip Memory Processors -- Techniques for Effectively Exploiting a Zero Overhead Loop Buffer -- Advanced Compiler Optimization for

CalmRISC8 Low-End Embedded Processor -- Global Software Pipelining with Iteration Preselection -- Analysis of Irregular Single-Indexed Array Accesses and Its Applications in Compiler Optimizations -- Advanced Scalarization of Array Syntax -- Techniques for Reducing the Overhead of Run-Time Parallelization -- MATOU: An Implementation of Mode-Automata -- Compiling Adaptive Programs by Partial Evaluation -- Functional Incremental Attribute Evaluation.

Sommario/riassunto

ETAPS2000 was the third instance of the European Joint Conferences on Theory and Practice of Software. ETAPS is an annual federated conference that was established in 1998 by combining a number of existing and new conferences. This year it comprised 5 conferences (FOSSACS, FASE, ESOP, CC, TACAS), 5 satellite workshops (CBS, CMCS, CoFI, GRATRA, INT), seven invited lectures, a panel discussion, and ten tutorials. The events that comprise ETAPS address various aspects of the system development process, including specification, design, implementation, analysis, and improvement. The languages, methodologies, and tools which support these activities are all well within its scope. Different blends of theory and practice are represented, with an inclination towards theory with a practical motivation on one hand and soundly-based practice on the other. Many of the issues involved in software design apply to systems in general, including hardware systems, and the emphasis on software is not intended to be exclusive.
