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| 1. Record Nr.           | UNISA996465559003316  |
| Titolo                  | Formal Methods in Computer-Aided Design [[electronic resource] ] : 4th International Conference, FMCAD 2002, Portland, OR, USA, November 6-8, 2002, Proceedings / / edited by Mark D. Aagaard, John W. O'Leary  |
| Pubbl/distr/stampa      | Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2002  |
| ISBN                    | 3-540-36126-X   |
| Edizione                | [1st ed. 2002.]   |
| Descrizione fisica      | 1 online resource (XII, 408 p.)   |
| Collana                 | Lecture Notes in Computer Science, , 0302-9743 ; ; 2517   |
| Disciplina              | 621.39/2  |
| Soggetti                | Computer-aided engineering<br>Computer hardware<br>Software engineering<br>Computer logic<br>Mathematical logic<br>Electrical engineering<br>Computer-Aided Engineering (CAD, CAE) and Design<br>Computer Hardware<br>Software Engineering<br>Logics and Meanings of Programs<br>Mathematical Logic and Formal Languages<br>Electrical Engineering  |
| Lingua di pubblicazione | Inglese   |
| Formato                 | Materiale a stampa  |
| Livello bibliografico   | Monografia  |
| Note generali           | Bibliographic Level Mode of Issuance: Monograph   |
| Nota di bibliografia    | Includes bibliographical references and index.  |
| Nota di contenuto       | Abstraction -- Abstraction by Symbolic Indexing Transformations -- Counter-Example Based Predicate Discovery in Predicate Abstraction -- Automated Abstraction Refinement for Model Checking Large State Spaces Using SAT Based Conflict Analysis -- Symbolic Simulation -- Simplifying Circuits for Formal Verification Using Parametric Representation -- Generalized Symbolic Trajectory Evaluation — Abstraction in Action -- Model Checking: Strongly-Connected Components -- Analysis of Symbolic SCC Hull Algorithms -- Sharp Disjunctive Decomposition for Language Emptiness Checking -- |

Microprocessor Specification and Verification -- Relating Multi-step and Single-Step Microprocessor Correctness Statements -- Modeling and Verification of Out-of-Order Microprocessors in UCLID -- Decision Procedures -- On Solving Presburger and Linear Arithmetic with SAT -- Deciding Presburger Arithmetic by Model Checking and Comparisons with Other Methods -- Qubos: Deciding Quantified Boolean Logic Using Propositional Satisfiability Solvers -- Model Checking: Reachability Analysis -- Exploiting Transition Locality in the Disk Based Mur? Verifier -- Traversal Techniques for Concurrent Systems -- Model Checking: Fixed Points -- A Fixpoint Based Encoding for Bounded Model Checking -- Using Edge-Valued Decision Diagrams for Symbolic Generation of Shortest Paths -- Verification Techniques and Methodology -- Mechanical Verification of a Square Root Algorithm Using Taylor's Theorem -- A Specification and Verification Framework for Developing Weak Shared Memory Consistency Protocols -- Model Checking the Design of an Unrestricted, Stuck-at Fault Tolerant, Asynchronous Sequential Circuit Using SMV -- Hardware Description Languages -- Functional Design Using Behavioural and Structural Components -- Compiling Hardware Descriptions with Relative Placement Information for Parametrised Libraries -- Prototyping and Synthesis -- Input/Output Compatibility of Reactive Systems -- Smart Play-out of Behavioral Requirements.

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## Sommario/riassunto

This volume contains the proceedings of the Fourth Biennial Conference on Formal Methods in Computer-Aided Design (FMCAD). The conference is devoted to the use of mathematical methods for the analysis of digital hardware circuits and systems. The work reported in this book describes the use of formal mathematics and associated tools to design and verify digital hardware systems. Functional verification has become one of the principal costs in a modern computer design effort. FMCAD provides a venue for academic and industrial researchers and practitioners to share their ideas and experiences of using concrete mathematical modeling and verification. Over the past 20 years, this area has grown from just a few academic researchers to a vibrant worldwide community of people from both academia and industry. This volume includes 23 papers selected from the 47 submitted papers, each of which was reviewed by at least three program committee members. The history of FMCAD dates back to 1984, when the earliest meetings on this topic occurred as part of IFIP WG10.2.

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| 2. Record Nr.           | UNICAMPANIAVAN0017437  |
| Autore                  | Tarpino, Antonella   |
| Titolo                  | Sentimenti del passato : la dimensione esistenziale del lavoro storico / Antonella Tarpino |
| Pubbl/distr/stampa      | Scandicci, : La nuova Italia, 1997   |
| ISBN                    | 88-221-2805-2  |
| Descrizione fisica      | IX, 336 p. ; 21 cm.  |
| Lingua di pubblicazione | Italiano   |
| Formato                 | Materiale a stampa   |
| Livello bibliografico   | Monografia   |