1. Record Nr. UNISA996465522603316 Algorithms and Architectures for Parallel Processing [[electronic **Titolo** resource]]: ICA3PP 2016 Collocated Workshops: SCDT, TAPEMS. BigTrust, UCER, DLMCS, Granada, Spain, December 14-16, 2016, Proceedings / / edited by Jesus Carretero, Javier Garcia-Blas, Victor Gergel, Vladimir Voevodin, Iosif Meyerov, Juan A. Rico-Gallego, Juan C. Díaz-Martín, Pedro Alonso, Juan Durillo, José Daniel Garcia Sánchez, Alexey L. Lastovetsky, Fabrizio Marozzo, Qin Liu, Zakirul Alam Bhuiyan, Karl Fürlinger, Josef Weidendorfer, José Gracia Pubbl/distr/stampa Cham:,: Springer International Publishing:,: Imprint: Springer,, 2016 **ISBN** 3-319-49956-4 Edizione [1st ed. 2016.] Descrizione fisica 1 online resource (XXV, 384 p. 126 illus.) Theoretical Computer Science and General Issues, , 2512-2029;; Collana 10049 Disciplina 004.35 Soggetti Algorithms Software engineering Computer networks Database management Application software Artificial intelligence Software Engineering **Computer Communication Networks Database Management** Computer and Information Systems Applications Artificial Intelligence Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Nota di contenuto Various dimensions of parallel algorithms and architectures --Fundamental theoretical approaches -- Practical experimental projects

> -- Commercial components and systems -- Beyond the limits of existing technologies -- Experimental efforts, innovative systems, and investigations -- identify weaknesses in existing parallel processing

technology.

Sommario/riassunto

This book constitutes the refereed workshop proceedings of the 16th International Conference on Algorithms and Architectures for Parallel Processing, ICA3PP 2016, held in Granada, Spain, in December 2016. The 30 full papers presented were carefully reviewed and selected from 58 submissions. They cover many dimensions of parallel algorithms and architectures, encompassing fundamental theoretical approaches, practical experimental projects, and commercial components and systems trying to push beyond the limits of existing technologies, including experimental efforts, innovative systems, and investigations that identify weaknesses in existing parallel processing technology.