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Collana	Lecture Notes in Computer Science, , 0302-9743 ; ; 2622
Disciplina	005.4/53
Soggetti	Programming languages (Electronic computers) Software engineering Computer logic Mathematical logic Artificial intelligence Programming Languages, Compilers, Interpreters Software Engineering Logics and Meanings of Programs Mathematical Logic and Formal Languages Artificial Intelligence
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references at the end of each chapters and index.
Nota di contenuto	Register Allocation -- Combined Code Motion and Register Allocation Using the Value State Dependence Graph -- Early Control of Register Pressure for Software Pipelined Loops -- Register Allocation by Optimal Graph Coloring -- Language Constructs and Their Implementation -- A Compilation and Optimization Model for Aspect-Oriented Programs -- A Pattern Matching Compiler for Multiple Target Languages -- A New One-Pass Transformation into Monadic Normal Form -- Type Analysis -- Run-Time Type Checking for Binary Programs -- Precision in Practice: A Type-Preserving Java Compiler -- The MAGICA Type Inference Engine for MATLAB ® -- CC Invited Talk -- Dimensions of

Precision in Reference Analysis of Object-Oriented Programming Languages -- Java -- Polyglot: An Extensible Compiler Framework for Java -- Scaling Java Points-to Analysis Using Spark -- Effective Inline-Threaded Interpretation of Java Bytecode Using Preparation Sequences -- Integrating Generations with Advanced Reference Counting Garbage Collectors -- Pot Pourri -- The Interprocedural Express-Lane Transformation -- Automatic Detection of Uninitialized Variables -- Generalised Regular Parsers -- Rapid and Robust Compiler Construction Using Template-Based Metacompilation -- ETAPS Invited Talk -- The Verifying Compiler: A Grand Challenge for Computing Research -- Optimization -- Address Register Assignment for Reducing Code Size -- Offset Assignment Showdown: Evaluation of DSP Address Code Optimization Algorithms -- Integrating High-Level Optimizations in a Production Compiler: Design and Implementation Experience -- Improving Data Locality by Chunking.

Sommario/riassunto

This book constitutes the refereed proceedings of the 12th International Conference on Compiler Construction, CC 2003, held in Warsaw, Poland, in April 2003. The 20 revised full regular papers and one tool demonstration paper presented together with two invited papers were carefully reviewed and selected from 83 submissions. The papers are organized in topical sections on register allocation, language constructs and their implementation, type analysis, Java, pot pourri, and optimization.
