1. Record Nr. UNISA996465479303316

Titolo	Tools and Algorithms for the Construction and Analysis of Systems [[electronic resource]] : First International Workshop, TACAS '95, Aarhus, Denmark, May 19 - 20, 1995. Selected Papers / / edited by Ed Brinksma, W. Rance Cleaveland, Kim G. Larsen, Tiziana Margaria, Bernhard Steffen
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 1995
ISBN	3-540-48509-0
Edizione	[1st ed. 1995.]
Descrizione fisica	1 online resource (VII, 298 p.)
Collana	Lecture Notes in Computer Science, , 0302-9743 ; ; 1019
Disciplina	005.2
Soggetti	Computer engineering Operating systems (Computers) Computers Computer logic Software engineering Computer communication systems Computer Engineering Operating Systems Theory of Computation Logics and Meanings of Programs Software Engineering Computer Communication Networks
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di contenuto	Combining model checking and deduction for I/O- automata -- A constraint oriented proof methodology based on modal transition systems -- A user guide to HyTech -- Modal ?-calculus, model checking and Gauß elimination -- Mona: Monadic second-order logic in practice -- Efficient simplification of bisimulation formulas -- Hierarchical compression for model-checking CSP or how to check 1020 dining philosophers for deadlock -- A front-end generator for

	verification tools -- Analytic and locally approximate solutions to properties of probabilistic processes -- Model checking of non-finite state processes by finite approximations -- On automatic and interactive design of communicating systems -- Layers as knowledge transitions in the design of distributed systems -- Parallelism for free: Bitvector analyses ? no state explosion!.
Sommario/riassunto	This book presents 12 revised refereed papers selected as the best from 32 submissions for the First International Workshop on Tools and Algorithms for the Construction and Analysis of Systems, TACAS '95, held in Aarhus, Denmark, in May 1995. The workshop brought together 46 researchers interested in the development and application of tools and algorithms for specification, verification, analysis, and construction of distributed systems. The papers included in the book are devoted to refinement-based and compositional verification, construction techniques, analysis and verification via theorem proving, process algebras, temporal and modal logics, techniques for real-time, hybrid and probabilistic systems, and value-passing systems.

2. Record Nr. UNINA9910767528503321

Titolo	Hardware and Software, Verification and Testing : Second International Haifa Verification Conference, HVC 2006, Haifa, Israel, October 23-26, 2006, Revised Selected Papers / / edited by Eyal Bin, Avi Ziv, Shmuel Ur
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2007
ISBN	1-280-86571-7 9786610865710 3-540-70889-8
Edizione	[1st ed. 2007.]
Descrizione fisica	1 online resource (XII, 235 p.)
Collana	Programming and Software Engineering, , 2945-9168 ; ; 4383
Disciplina	004
Soggetti	Software engineering Computer science Compilers (Computer programs) Software Engineering Computer Science Logic and Foundations of Programming Compilers and Interpreters
Lingua di pubblicazione	Inglese

Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Hardware Verification Track -- Model Checking PSL Using HOL and SMV -- Using Linear Programming Techniques for Scheduling-Based Random Test-Case Generation -- Extracting a Simplified View of Design Functionality Based on Vector Simulation -- Automatic Fault Localization for Property Checking -- Verification of Data Paths Using Unbounded Integers: Automata Strike Back -- Tools Track -- Smart-Lint: Improving the Verification Flow -- Model-Driven Development with the jABC -- Detecting Design Flaws in UML State Charts for Embedded Software -- A Panel: Unpaved Road Between Hardware Verification and Software Testing Techniques -- An Open Source Simulation Model of Software Development and Testing -- Software Testing Track -- ExpliSAT: Guiding SAT-Based Software Verification with Explicit States -- Evolutionary Testing: A Case Study -- A Race-Detection and Flipping Algorithm for Automated Testing of Multi-threaded Programs -- Explaining Intermittent Concurrent Bugs by Minimizing Scheduling Noise -- Testing the Machine in the World -- Choosing a Test Modeling Language: A Survey -- Making Model-Based Testing More Agile: A Use Case Driven Approach.
Sommario/riassunto	The Haifa Verification Conference 2006 took place for the second year in a row at the IBM Haifa Research Lab and at the Haifa University in Israel during October 23–26, 2006. The verification conference was a three-day, single-track conference followed by a one-day tutorial on PSL. This Haifa Verification Conference was established to bring together researchers from two different disciplines, hardware verification and software testing. The use of similar techniques among the two communities enabled the conference to help generate a unique synergy that fortifies both groups. This year, we had two traditional tracks, hardware verification and software testing, in addition to a new track dedicated to tools in these areas. The conference emphasized applicability to real-world challenges, which was vital to the many attendees coming from industry. The conference hosted two internationally recognized individuals as keynote speakers. Randal E. Bryant, Dean and University Professor from the School of Computer Science at Carnegie Mellon University gave a talk on “System Modeling and Formal Verification with UCLID” and Michael Jackson from the University of Newcastle gave a talk on “Testing the Machine in the World. ” The numerous invited speakers presented topics of great interest to the audience. Just some of these outstanding speakers included Cindy Eisner in the hardware verification track, Alon Gluska and Andrew Piziali in the tools track, and Mauro Pezze and Nir Shavit in the software testing track.