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Titolo	Algorithms and Architectures for Parallel Processing [[electronic resource]] : 12th International Conference, ICA3PP 2012, Fukuoka, Japan, September 4-7, 2012, Proceedings, Part II // edited by Yang Xiang, Ivan Stojmenovic, Bernady O. Apduhan, Guojun Wang, Koji Nakano, Albert Y. Zomaya
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Descrizione fisica	1 online resource (XXIV, 331 p. 159 illus.)
Collana	Theoretical Computer Science and General Issues, , 2512-2029 ; ; 7440
Disciplina	005.1
Soggetti	Algorithms Artificial intelligence Software engineering Application software Computer networks Electronic data processing—Management Artificial Intelligence Software Engineering Computer and Information Systems Applications Computer Communication Networks IT Operations
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	International conference proceedings.
Nota di bibliografia	Includes bibliographical references and author index.
Nota di contenuto	Analytical Modeling for Multi-transaction Bus on Distributed Systems -- Leveraging the Strengths of Transactional Memory While Maintaining System Performance for a Multiplayer Gaming -- Enhancing the Performance of a Distributed Mobile Computing Environment by Topology Construction -- Frame Error Rate Testing for High Speed Optical -- Maintaining Consistency in Software Transactional Memory through Dynamic Versioning Tuning -- Solving a 2-Covered Path Problem with Variable Radii for Wireless Sensor Networks -- Design of an Application-Dependent Static-Based Shared Memory Network --

Determining Quality of S-Boxes Using Pseudo Random Sequences
Generated from Stream Ciphers -- Exploring Object-Level Parallelism
on Chip Multi-processors -- The Hamiltonicity of WK-Recursive
Pyramid -- Towards Multi-level Adaptation for Distributed Operating
Systems and Applications -- FIDs Classifier for Artificial Intelligence
and Its -- On Affirmative Adaptive Failure Detection -- A New Low
Latency Parallel Turbo Decoder Employing Parallel Phase Decoding
Method -- A Regular Group Quorum System of Degree A Note on
Developing Optimal and Scalable Parallel Two-List Algorithms -- Small
Business-Oriented Index Construction of Cloud Data -- High-
Performance Matrix Multiply on a Massively Multithreaded Fiteng1000
Processor -- BIDE-Based Parallel Mining of Frequent Closed Sequences
with MapReduce -- Efficient Task Scheduling for Hard Real-Time Tasks
in Asymmetric Multicore Processors -- An Implementation of Parallel 2-
D FFT Using Intel AVX Instructions on Multi-core Processors -- A
Semantic Impact in Decentralized Resource Discovery Mechanism for
Grid Computing Environments -- Design of n-Gram Based Dynamic
Pre-fetching for DSM -- On Construction of Cloud IaaS for VM Live
Migration Using KVM and OpenNebula -- Performance Evaluation of
OpenMP and CUDA on Multicore -- Complexity of the Resource
Allocation/Matching Problem with Weight Based Ceilings -- Wireless
Sensor Network Internal Attacker Identification with Multiple Evidence
by Dempster-Shafer Theory -- A Novel Feature Selection Scheme for
Energy Efficient Wireless Sensor Networks -- A Comprehensive Survey
of the Feature Extraction Methods in the EEG Research -- Development
of a Smart e-Health Portal for Chronic Disease Management -- A Novel
Approach to Protein Structure Prediction Using PCA Based Extreme
Learning Machines and Multiple Kernels -- A Novel Approach to
Guarantee Causal Message Ordering in Pre-planned Wireless Sensor
Networks -- People Identification with RMS-Based Spatial Pattern of
EEG Signal -- Gait Based Human Identity Recognition from Multi-view
Surveillance Videos.

Sommario/riassunto

The two volume set LNCS 7439 and 7440 comprises the proceedings of the 12th International Conference on Algorithms and Architectures for Parallel Processing, ICA3PP 2012, as well as some workshop papers of the CDCN 2012 workshop which was held in conjunction with this conference. The 40 regular paper and 26 short papers included in these proceedings were carefully reviewed and selected from 156 submissions. The CDCN workshop attracted a total of 19 original submissions, 8 of which are included in part II of these proceedings. The papers cover many dimensions of parallel algorithms and architectures, encompassing fundamental theoretical approaches, practical experimental results, and commercial components and systems.
