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Soggetti	Computer input-output equipment Computer systems Operating systems (Computers) Computer programming Compilers (Computer programs) Logic design Input/Output and Data Communications Computer System Implementation Operating Systems Programming Techniques Compilers and Interpreters Logic Design
Lingua di pubblicazione	Inglese
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Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Workshop on Virtualization in High-Performance Cloud Computing (VHPC) -- Visualization at Scale: Deployment Case Studies and Experience Reports -- International Workshop on Performance Portable Programming Models for Accelerators (P ³ MA) -- OpenPOWER for HPC (IWOPH) -- International Workshop on Data Reduction for Big Scientific Data (DRBSD) -- International Workshop on Communication

Architectures for HPC, Big Data, Deep Learning and Clouds at Extreme Scale -- Workshop on HPC Computing in a Post Moore's Law World (HCPM) -- HPC I/O in the Data Center (HPC-IODC) -- Workshop on Performance and Scalability of Storage Systems (WOPSSS) -- Experiences on Intel Knights Landing at the One Year Mark (IXPUG) -- International Workshop on Communication Architectures for HPC, Big Data, Deep Learning and Clouds at Extreme Scale (ExaComm).

Sommario/riassunto

This book constitutes revised selected papers from 10 workshops that were held as the ISC High Performance 2017 conference in Frankfurt, Germany, in June 2017. The 59 papers presented in this volume were carefully reviewed and selected for inclusion in this book. They stem from the following workshops: Workshop on Virtualization in High-Performance Cloud Computing (VHPC) Visualization at Scale: Deployment Case Studies and Experience Reports International Workshop on Performance Portable Programming Models for Accelerators (P³MA) OpenPOWER for HPC (IWOPH) International Workshop on Data Reduction for Big Scientific Data (DRBSD) International Workshop on Communication Architectures for HPC, Big Data, Deep Learning and Clouds at Extreme Scale Workshop on HPC Computing in a Post Moore's Law World (HCPM) HPC I/O in the Data Center (HPC-IODC) Workshop on Performance and Scalability of Storage Systems (WOPSSS) IXPUG: Experiences on Intel Knights Landing at the One Year Mark International Workshop on Communication Architectures for HPC, Big Data, Deep Learning and Clouds at Extreme Scale (ExaComm).
