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Nota di contenuto	Title Page; Copyright; Table of Contents; List of Contributors; Preface; Acknowledgments; Chapter 1: Fuels and Combustion; 1.1 Introduction; 1.2 The Options; 1.3 Spark Ignition; 1.4 Compression Ignition; 1.5 Highly Diluted Autoignition, HCCI; 1.6 Other Combustion Concepts; 1.7 Summary of Combustion Processes; References; Chapter 2: Fuel Class Higher Alcohols; 2.1 Introduction and Fuel Properties; 2.2 Performance in Spark-Ignition Engines; 2.3 Performance in Compression-Ignition Engines; 2.4 Production Pathways; 2.5 Outlook; 2.6 Conclusions; References; Chapter 3: Fuel Class Valerates 3.1 Introduction and Fuel Properties3.2 Performance in Spark-Ignition Engines; 3.3 Performance in Compression-Ignition Engines; 3.4 Production Pathways; 3.5 Outlook; 3.6 Conclusions; Acknowledgments; References; Chapter 4: Butyl Ethers and Levulinates; 4.1 Introduction and Fuel Properties; 4.2 Performance in Compression-Ignition Engines; 4.3 Production Pathways; 4.4 Outlook; 4.5 Conclusions; References; Chapter 5: A Comprehensive Review of 2,5-Dimethylfuran as a Biofuel Candidate; 5.1 Introduction to DMF; 5.2 Production Pathways; 5.3 Performance in Spark-Ignition Engines 5.4 Performance in Compression-Ignition Engines5.5 Outlook; 5.6

Conclusions; References; Chapter 6: Furanoids; 6.1 Introduction and Fuel Properties; 6.2 Performance in Spark-Ignition Engines; 6.3 Performance in Compression-Ignition Engines; 6.4 Production Pathways; 6.5 Outlook; 6.6 Conclusions; References; Chapter 7: Benzenoids; 7.1 Introduction; 7.2 Overview of Neat Fuel properties; 7.3 Performance in Compression-Ignition Engines; 7.4 Performance in Spark-Ignition Engines; 7.5 Production Pathways; 7.6 Outlook and Conclusions; References; Chapter 8: Biomass Pyrolysis Oils 8.1 Introduction and Fuel Properties 8.2 Performance Spark-Ignition Engines; 8.3 Performance in Compression-Ignition Engines; 8.4 Production Pathways from Pyrolysis Oil; 8.5 Outlook; 8.6 Conclusions; References; Index; End User License Agreement

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Preprocessing and Inprocessing Techniques in SAT -- Pioneering the Future of Verification: A Spiral of Technological and Business Innovation -- Automated Detection and Repair of Concurrency Bugs -- Verification Challenges of Workload Optimized Hardware Systems -- Synthesis with Clairvoyance -- Generalized Reactivity(1) Synthesis without a Monolithic Strategy -- IIS-Guided DFS for Efficient Bounded Reachability Analysis of Linear Hybrid Automata -- Cube and Conquer: Guiding CDCL SAT Solvers by Lookaheads. Implicative Simultaneous Satisfiability and Applications -- Liveness vs Safety – A Practical Viewpoint -- Predicting Serializability Violations: SMT-Based Search vs. DPOR-Based Search -- SAM: Self-adaptive Dynamic Analysis for Multithreaded Programs -- Concurrent Small Progress Measures -- Specification and Quantitative Analysis of Probabilistic Cloud Deployment Patterns -- Interpolation-Based Function Summaries in Bounded Model Checking -- Can File Level Characteristics Help Identify System Level Fault-Proneness -- Reverse Coverage Analysis -- Symbolic Testing of OpenCL Code -- Dynamic Test Data Generation for Data Intensive Applications -- Injecting Floating-Point Testing Knowledge into Test Generators -- Combining Theorem Proving and Symbolic Trajectory Evaluation in THM&STE -- HAVEN: An Open Framework for FPGA-Accelerated Functional Verification of Hardware -- On-Line Detection and Prediction of Temporal Patterns -- Function Summaries in Software Upgrade Checking -- The Rabin Index of Parity Games -- Using Computational Biology Methods to Improve Post-silicon Microprocessor Testing. -- Pioneering the Future of Verification: A Spiral of Technological and Business Innovation -- Automated Detection and Repair of Concurrency Bugs -- Verification Challenges of Workload Optimized Hardware Systems -- Synthesis with Clairvoyance -- Generalized Reactivity(1) Synthesis without a Monolithic Strategy -- IIS-Guided DFS for Efficient Bounded Reachability Analysis of Linear Hybrid Automata -- Cube and Conquer: Guiding CDCL SAT Solvers by Lookaheads. Implicative Simultaneous Satisfiability and Applications -- Liveness vs Safety – A Practical Viewpoint -- Predicting Serializability Violations: SMT-Based Search vs. DPOR-Based Search -- SAM: Self-adaptive Dynamic Analysis for Multithreaded Programs -- Concurrent Small Progress Measures -- Specification and Quantitative Analysis of Probabilistic Cloud Deployment Patterns -- Interpolation-Based Function Summaries in Bounded Model Checking -- Can File Level Characteristics Help Identify System Level Fault-Proneness -- Reverse Coverage Analysis -- Symbolic Testing of OpenCL Code -- Dynamic Test Data Generation for Data Intensive Applications -- Injecting Floating-Point Testing Knowledge into Test Generators -- Combining Theorem Proving and Symbolic Trajectory Evaluation in THM&STE -- HAVEN: An Open Framework for FPGA-Accelerated Functional Verification of Hardware -- On-Line Detection and Prediction of Temporal Patterns -- Function Summaries in Software Upgrade Checking -- The Rabin Index of Parity Games -- Using Computational Biology Methods to Improve Post-silicon Microprocessor Testing.

This book constitutes the thoroughly refereed post-conference proceedings of the 7th International Haifa Verification Conference, HVC 2011, held in Haifa, Israel in December 2011. The 15 revised full papers presented together with 3 tool papers and 4 posters were carefully reviewed and selected from 43 submissions. The papers are organized in topical sections on synthesis, formal verification, software quality, testing and coverage, experience and tools, and posters-student event.