1.	Record Nr.	UNISA996418205003316
	Titolo	Verification, Model Checking, and Abstract Interpretation [[electronic resource]] : 21st International Conference, VMCAI 2020, New Orleans, LA, USA, January 16–21, 2020, Proceedings / / edited by Dirk Beyer, Damien Zufferey
	Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2020
	ISBN	3-030-39322-4
	Edizione	[1st ed. 2020.]
	Descrizione fisica	1 online resource (XIV, 471 p. 109 illus.)
	Collana	Theoretical Computer Science and General Issues, , 2512-2029 ; ; 11990
	Disciplina	005.14
	Soggetti	Computer science
		Computer engineering
		Computer networks
		Logic programming
		Artificial intelligence - Data processing
		Software engineering
		Computer Science Logic and Foundations of Programming
		Computer Engineering and Networks
		Logic in AI
		Data Science
		Software Engineering
	Lingua di pubblicazione	Inglese
	Formato	Materiale a stampa
	Livello bibliografico	Monografia
	Nota di contenuto	Witnessing Secure Compilation -- BackFlow: Backward Context-sensitive Flow Reconstruction of Taint Analysis Results -- Fixing Code That Explodes Under Symbolic Evaluation -- The Correctness of a Code Generator for a Functional Language -- Leveraging Compiler Intermediate Representation for Multi- and Cross-Language Verification -- Putting the Squeeze on Array Programs: Loop Verification via Inductive Rank Reduction -- A Systematic Approach to Abstract Interpretation of Program Transformations -- Sharing ghost variables in a collection of abstract domains -- Harnessing Static

Analysis to Help Learn Pseudo-Inverses of String Manipulating Procedures for Automatic Test Generation -- Synthesizing Environment Invariants for Modular Hardware Verification -- Systematic Classi cation of Attackers via Bounded Model Checking -- Cheap CTL Compassion in NuSMV -- A Cooperative Parallelization Approach for Property-Directed k-Induction -- Generalized Property-Directed Reachability for Hybrid Systems -- Language Inclusion for Finite Prime Event Structures -- Promptness and Bounded Fairness in Concurrent and Parameterized Systems -- Solving LIA* Using Approximations -- Formalizing and checking Multilevel Consistency -- Practical Abstractions for Automated Veri cation of Shared-Memory Concurrency -- How to Win First-Order Safety Games -- Improving Parity Game Solvers with Justifications.

Sommario/riassunto	This book constitutes the proceedings of the 21st International Conference on Verification, Model Checking, and Abstract Interpretation, VMCAI 2020. The 21 papers presented in this volume were carefully reviewed from 44 submissions. VMCAI provides a forum for researchers from the communities of verification, model checking, and abstract Interpretation, facilitating interaction, cross-fertilization, and advancement of hybrid methods that combine these and related areas. .