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Autore	Comber Thomas <1645-1699.>
Titolo	The occasional offices of matrimony, visitation of the sick, burial of the dead, churching of women, and the commination explained in the method of the Companion to the temple [[electronic resource]] : being the fourth and last part / / by Thomas Comber .
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Autore	Rafiquzzaman Mohamed
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Nota di contenuto	Microprocessor Theory and Applications with 68000/68020 and Pentium; CONTENTS; PREFACE; CREDITS; 1. INTRODUCTION TO MICROPROCESSORS; 1.1 Explanation of Terms; 1.2 Microprocessor Data Types; 1.2.1 Unsigned and Signed Binary Numbers; 1.2.2 ASCII and EBCDIC Codes; 1.2.3 Unpacked and Packed Binary-Coded-Decimal Numbers; 1.2.4 Floating-point Numbers; 1.3 Evolution of the Microprocessor; 1.4 Typical Features of 32-bit and 64-bit Microprocessors; 1.5 Microprocessor-based System Design Concepts; 1.6 Typical Microprocessor Applications; 1.6.1 A Simple Microprocessor Application 1.6.2 Examples of Typical Microprocessor Applications2. MICROCOMPUTER ARCHITECTURE; 2.1 Basic Blocks of a Microcomputer; 2.2 Typical Microcomputer Architecture; 2.2.1 System Bus; 2.2.2 Clock Signals; 2.3 Single-Chip Microprocessor; 2.3.1 Register Section; 2.3.2 Control Unit; 2.3.3 Arithmetic-Logic Unit; 2.3.4 Functional Representations of Simple and Typical Microprocessors; 2.3.5 Simplified Explanation of Control Unit design; 2.4 Program Execution

by Conventional Microprocessors; 2.5 Program Execution by typical 32-bit Microprocessors; 2.5.1 Pipelining; 2.5.2 Branch Prediction Feature
2.6 Scalar and Superscalar Microprocessors2.7 RISC vs. CISC; Questions and Problems; 3. MICROPROCESSOR MEMORY ORGANIZATION; 3.1 Introduction; 3.2 Main memory; 3.2.1 Read-Only Memory; 3.2.2 Random-Access Memory; 3.2.3 READ and WRITE Timing Diagrams; 3.2.4 Main Memory Organization; 3.2.5 Main Memory Array Design; 3.3 Microprocessor on-chip memory management unit and cache; 3.3.1 Memory Management Concepts; 3.3.2 Cache Memory Organization; Questions and Problems; 4. MICROPROCESSOR INPUT/OUTPUT; 4.1 Introduction; 4.2 Simple I/O Devices; 4.3 Programmed I/O
4.4 Unconditional and Conditional Programmed I/O4.5 Interrupt I/O; 4.5.1 Interrupt Types; 4.5.2 Interrupt Address Vector; 4.5.3 Saving the Microprocessor Registers; 4.5.4 Interrupt Priorities; 4.6 Direct Memory Access (DMA); 4.7 Summary of I/O; Questions and Problems; 5. MICROPROCESSOR PROGRAMMING CONCEPTS; 5.1 Microcomputer Programming Languages; 5.2 Machine Language; 5.3 Assembly Language; 5.3.1 Types of Assemblers; 5.3.2 Assembler Delimiters; 5.3.3 Specifying Numbers by Typical Assemblers; 5.3.4 Assembler Directives or Pseudoinstructions; 5.3.5 Assembly Language Instruction Formats
5.3.6 Instruction Set Architecture (ISA)5.3.7 Typical Instruction Set; 5.3.8 Typical Addressing Modes; 5.3.9 Subroutine Calls in Assembly Language; 5.4 High-Level Language; 5.5 Choosing a programming language; 5.6 Flowcharts; Questions and Problems; 6. ASSEMBLY LANGUAGE PROGRAMMING WITH THE 68000; 6.1 Introduction; 6.2 68000 Registers; 6.3 68000 Memory Addressing; 6.4 Assembly Language Programming with the 68000; 6.5 68000 Addressing Modes; 6.5.1 Register Direct Addressing; 6.5.2 Address Register Indirect Addressing; 6.5.3 Absolute Addressing; 6.5.4 Program Counter Relative Addressing
6.5.5 Immediate Data Addressing

Sommario/riassunto

A self-contained introduction to microprocessor theory and applications This book presents the fundamental concepts of assembly language programming and system design associated with typical microprocessors, such as the Motorola MC68000/68020 and Intel? Pentium?. It begins with an overview of microprocessors--including anexplanation of terms, the evolution of the microprocessor, and typical applications--and goes on to systematically cover: Microcomputer architectureMicroprocessor memory organizationMicroprocessor Input/Output (I/O)Microprocessor programming concept