1. Record Nr. UNISA996397191503316

Titolo	Two letters from His Excellency Sir Thomas Fairfax [[electronic resource]] : one to both Houses of Parliament, giving an accompt of what transactions and proceedings have been betwixt the Kings Majesty and the Army, since his coming into their quarters : with some farther proposals in relation to His Majesty, and the speedy settlement of the peace of the Kingdom : the other a letter to the Lord Major, aldermen and Common-Councel of the city of London : with some papers of the proceedings of the treaty with the Army
Pubbl/distr/stampa	London, : Printed for Laurence Chapman, Ivly 10, 1647
Descrizione fisica	11 p
Altri autori (Persone)	FairfaxThomas Fairfax, Baron, <1612-1671.>
Soggetti	Great Britain History Civil War, 1642-1649 Early works to 1800 Great Britain Politics and government 1642-1649 Early works to 1800
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Reproduction of original in: Sutro Library.
Sommario/riassunto	eebo-0111

2. Record Nr. UNISA996395763803316

Autore	P. I, Minister
Titolo	A letter written by a minister for the satisfaction of a person doubting in religion [[electronic resource]] : shewn to be unsatisfactory
Pubbl/distr/stampa	London, : Printed by Henry Hills ..., 1686
Descrizione fisica	[8], 38, [i.e. 30] p
Altri autori (Persone)	T. B J. W
Soggetti	Transubstantiation
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	A defense of Catholicism. "To the reader" signed: P.I. Text of the letter, "Mr. T.B.'s paper to J.W.," dated Preston, Aug. 3, 1686: p. 1-8. Reproduction of original in Union Theological Seminary Library, New York.
Sommario/riassunto	eebo-0160

3. Record Nr. UNINA9910260635803321

Autore	Pedroni Volnei A
Titolo	Circuit design with VHDL / / Volnei A. Pedroni
Pubbl/distr/stampa	Cambridge, Mass., : MIT Press, c2004
ISBN	1-282-09667-2 0-262-25678-9
Descrizione fisica	xii, 363 p. : ill
Disciplina	621.39/5
Soggetti	VHDL (Computer hardware description language) Electronic circuit design System design
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Title from title screen.
Nota di bibliografia	Includes bibliographical references (p. [357]) and index.
Sommario/riassunto	This textbook teaches VHDL using system examples combined with programmable logic and supported by laboratory exercises. While other textbooks concentrate only on language features, Circuit Design with VHDL offers a fully integrated presentation of VHDL and design concepts by including a large number of complete design examples, illustrative circuit diagrams, a review of fundamental design concepts, fully explained solutions and simulation results. The text presents the information concisely yet completely, discussing in detail all indispensable features of the VHDL synthesis. The book is organised in a clear progression, with the first part covering the circuit level, treating foundations of VHDL and fundamental coding, and the second part covering the system level (units that might be located in a library for code sharing, reuse and partitioning), expanding upon the earlier chapters to discuss system coding. techniques of VHDL, including code structure, data types, operators and attributes, concurrent and sequential statements and code, objects (signals, variables and constants), design of finite state machines and examples of additional circuit designs. Part II, System Design, builds on the material already presented, adding elements intended mainly for library allocation; it examines packages and components, functions and procedures and

additional examples of system design. Appendixes on programmable logic devices (PLDs/FPGAs) and synthesis tools follow Part II. The book's highly original approach of teaching through extensive system examples as well as its unique integration of VHDL and design make it suitable both for use by students in computer science and electrical engineering.