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Introduction; 4.2 Instrumentation amplifiers; 4.3 Inverting summing amplifiers; 4.4 Non-inverting summing amplifiers; 4.5 Integrators; 4.6 Charge amplifiers; 4.7 Precision rectifiers; References; Chapter 5. Diode and transistor circuits; 5.1 Semiconductor diodes; 5.2 Diode characteristics; 5.3 Bipolar junction transistors (BJTs); 5.4 Bipolar junction transistor parameters and amplifiers; 5.5 Field-effect transistors (FETs); References; Chapter 6. Design of operation amplifiers ('op amps'); 6.1 Structure of the op amp 6.2 The differential-pair input stage 6.3 The second stage and the output stage; 6.4 The constant-current sources; 6.5 Common-mode rejection ratio (CMRR); 6.6 Frequency response; 6.7 Slew rate; 6.8 Integrated-circuit op amps; 6.9 Radio-frequency (r.f.) op amps; 6.10 Video amplifiers; References; Chapter 7. Analog-to-digital and digital-to-analog conversion; 7.1 Introduction; 7.2 Quantization; 7.3 Sampling; 7.4 Analog-to-digital conversion; 7.5 Analog-to-digital converters; 7.6 Digital-to-analog converters; 7.7 Errors in A-D and D-A converters; References
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10.2 A simple LCR filter

Sommario/riassunto

The content has been carefully designed to meet the requirements of first and second year students of electronic engineering, communications engineering and telecommunications, following full honours degree programs or two-year courses including HNC/HND. A companion website includes interactive spreadsheets to download.*A completely new analog electronics textbook for the digital age*Coverage ideal for courses with a communications / wireless focus*Companion website provides interactive spreadsheets, where readers can put the book's theory into practice

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