

1. Record Nr.	UNISA996391204603316
Autore	Kerswell John
Titolo	Speculum gratitudinis; or, David's thankfulness unto God for all His benefits [[electronic resource]] : Expressed in a sermon on the 29th of May, 1664. being Whitsunday, and the day of the happy birth and return of our Gracious King, Charles the Second. By John Kerswel, B.D. and rector of Goddington in Oxford-shire
Pubbl/distr/stampa	London, : printed for the author, 1665
Descrizione fisica	[32] p
Soggetti	Sermons, English - 17th century
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Running title reads: Speculum gratitudinis; or, David's thankfulness. Many errors in pagination. Signatures: A-Dâ´. Many pages numbered in MS; final page numbered 131; changed to "141" in MS. Reproduction of the original in the British Library.
Sommario/riassunto	eebo-0018

2. Record Nr.	UNINA9911006817303321
Autore	Hennessy John L
Titolo	Computer architecture [[electronic resource]] : a quantitative approach // John L. Hennessy, David A. Patterson ; with contributions by Andrea C. Arpaci-Dusseau ... [et al.]
Pubbl/distr/stampa	Amsterdam ; ; Boston, : Morgan Kaufmann, 2007
ISBN	1-282-33002-0 9786612330025 0-08-047502-7
Edizione	[4th ed.]
Descrizione fisica	1 online resource (705 p.)
Collana	The Morgan Kaufmann Series in Computer Architecture and Design
Altri autori (Persone)	PattersonDavid A Arpaci-DusseauAndrea C
Disciplina	004.2/2 22 004.22
Soggetti	Computer architecture
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Front cover; About the Authors; Copyright page; Topic Selection and Organization; An Overview of the Content; Navigating the Text; Chapter Structure; Case Studies with Exercises; Supplemental Materials; Helping Improve This Book; Concluding Remarks; Contributors to Previous Editions
Sommario/riassunto	The era of seemingly unlimited growth in processor performance is over: single chip architectures can no longer overcome the performance limitations imposed by the power they consume and the heat they generate. Today, Intel and other semiconductor firms are abandoning the single fast processor model in favor of multi-core microprocessors--chips that combine two or more processors in a single package. In the fourth edition of Computer Architecture, the authors focus on this historic shift, increasing their coverage of multiprocessors and exploring the most effective ways of achieving par