

1. Record Nr.	UNISA996390749703316
Autore	Dyke Daniel <d. 1614.>
Titolo	The mystery of selfe-deceiuing. Or A discourse and discouery of the deceitfulnesse of mans heart. Written by the late faithfull minister of Gods word Daniell Dyke, Bachelour in Diuinity. Published since his death, by his brother I.D. minister of Gods word [[electronic resource]]
Pubbl/distr/stampa	London, : Printed by Edvvard Griffin for Ralph Mab, and are to be sold at the signe of the Grey-hound in Pauls Church-yard, 1615
Descrizione fisica	[16], 106, 105-126, 117-116 [i.e. 216], 227-234, 225-426, [2] p
Altri autori (Persone)	DykeJeremiah <1584-1639.>
Soggetti	Sin
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	I.D. = Jeremiah Dyke. P. 216 misnumbered 116. Text continuous despite pagination. The last leaf is blank. Running title reads: The deceitfulnesse of mans heart. Reproduction of the original in the British Library.
Sommario/riassunto	eebo-0018

2. Record Nr.	UNISA996466116403316
Titolo	Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation [[electronic resource]] : 16th International Workshop, PATMOS 2006, Montpellier, France, September 13-15, 2006, Proceedings // edited by Johan Vounckx, Nadine Azemard, Philippe Maurine
Pubbl/distr/stampa	Berlin, Heidelberg : , : Springer Berlin Heidelberg : , : Imprint : Springer, , 2006
ISBN	3-540-39097-9
Edizione	[1st ed. 2006.]
Descrizione fisica	1 online resource (XVI, 677 p.)
Collana	Theoretical Computer Science and General Issues, , 2512-2029 ; ; 4148
Classificazione	SS 4800
Disciplina	621.395
Soggetti	Computer science Logic design Microprocessors Computer architecture Electronic digital computers—Evaluation Computer arithmetic and logic units Computer storage devices Memory management (Computer science) Theory of Computation Logic Design Processor Architectures System Performance and Evaluation Arithmetic and Logic Structures Computer Memory Structure
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	Session 1 – High-Level Design -- Session 2 – Power Estimation / Modeling -- Session 3 – Memory and Register Files -- Session 4 – Low-Power Digital Circuits -- Session 5 – Busses and Interconnects -- Session 6 – Low Power Techniques -- Session 7 – Applications and SoC Design -- Session 8 – Modeling -- Session 9 – Digital Circuits --

Sommario/riassunto

Welcome to the proceedings of PATMOS 2006, the 16th in a series of international workshops. PATMOS 2006 was organized by LIRMM with CAS technical - sponsorship and CEDA sponsorship. Over the years, the PATMOS workshop has evolved into an important European event, where researchers from both industry and academia discuss and investigate the emerging challenges in future and contemporary applications, design methodologies, and tools required for the development of upcoming generations of integrated circuits and systems. The technical program of PATMOS 2006 contained state-of-the-art technical contributions, three invited talks, a special session on hearing-aid design, and an embedded tutorial. The technical program focused on timing, performance and power consumption, as well as architectural aspects with particular emphasis on modeling, design, characterization, analysis and optimization in the nanometer era. The Technical Program Committee, with the assistance of additional expert reviewers, selected the 64 papers presented at PATMOS. The papers were organized into 11 technical sessions and 3 poster sessions. As is always the case with the PATMOS workshops, full papers were required, and several reviews were received per manuscript.

3. Record Nr.	UNINA9910163214003321
Autore	Orr III USMC Lt. - Alan L
Titolo	George Washington
Pubbl/distr/stampa	TBD : , : Pickle Partners Publishing, , 2014 ©2014
ISBN	9781782896487 1782896481
Edizione	[1st ed.]
Descrizione fisica	1 online resource (23 p.)
Disciplina	973.41
Soggetti	Military biography Strategy - History United States
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di contenuto	Title page -- TABLE OF CONTENTS -- ABSTRACT -- GEORGE WASHINGTON: AMERICA'S FIRST STRATEGIC LEADER -- The Formative Years -- Preparation for Command -- Commander of the Continental Army -- After the War -- Conclusion
Sommario/riassunto	George Washington is widely recognized as one of the greatest strategic leaders in our nation's history. His ability to lead a rag-tag group of militia against the most powerful nation of his time appears to be unexplainable. Through further analysis though, one can begin to see a pattern appear that may explain why Washington's personal theory of war was so successful, and hence explain why he became such a great strategic leader. George Washington was not a particularly successful tactical leader, and his experiences in leading troops culminated prior to the Revolutionary War at the Regimental level. He went on to lead a productive life as a statesman in the Virginia legislature until the war with Britain erupted and he was cast into the role as America's first Commander in Chief. His ability to comprehend the conflict for what it was, as well as his ability to understand the will of his fellow countrymen allowed him to craft a wartime strategy for victory against the most powerful nation on earth at the time. He kept the will of the people, the tactics of the army and the desires of the

state in balance to devise a strategy that would allow him to go down in history as America's first strategic leader.
