

1. Record Nr.	UNINA9910450039003321
Autore	Geerts Yves
Titolo	Design of multi-bit delta-sigma A/D converters
Pubbl/distr/stampa	Boston, MA : , : Springer US, , 2002
ISBN	0-306-48015-8
Descrizione fisica	1 online resource (X, 226 p.)
Collana	SECS Design of multi-bit delta-sigma A/D converters
Disciplina	621.39/814
Soggetti	Analog-to-digital converters - Design and construction Metal oxide semiconductors, Complementary - Design and construction Broadband communication systems - Equipment and supplies - Design and construction Very high speed integrated circuits - Design and construction Electrical & Computer Engineering Engineering & Applied Sciences Electrical Engineering Electronic books.
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph
Nota di contenuto	Architecture Study of Delta-Sigma Converters -- Design Considerations for Multi-Bit ?? Converters -- Implementations -- Conclusions.
Sommario/riassunto	Design of Multi-Bit Delta-Sigma A/D Converters discusses both architecture and circuit design aspects of Delta-Sigma A/D converters, with a special focus on multi-bit implementations. The emphasis is on high-speed high-resolution converters in CMOS for ADSL applications, although the material can also be applied for other specification goals and technologies. Design of Multi-Bit Delta-Sigma A/D Converters starts with a general introduction of the concepts of Delta-Sigma converters. A wide variety of architectures are discussed, ranging from single-loop to cascaded and various multi-bit topologies. These topologies are optimized to obtain stable converters with a high accuracy. A clear overview is provided of the maximum achievable performance of each topology, which allows a designer to select the optimal architecture for a certain specification. Special attention is paid

to multi-bit architectures and possible solutions for the linearity problem of the DA converter in the feedback loop of converters. Several circuit design aspects of multi-bit Delta-Sigma converters are discussed. Various models are provided for a wide range of linear and non-linear circuit imperfections, which can degrade the performance of the converter. These models allow the designer to determine the required specifications for the different building blocks and form the basis of a systematic design procedure. The presented material is combined in a concluding chapter, which illustrates the systematic design procedure for two high-performance converters. Design of Multi-Bit Delta-Sigma A/D Converters provides a clear comparison of architectures and yields insight into the influence of the most important circuit non-idealities. It will allow you to design robust and high-performance Delta-Sigma AD converters in a shorter time. It is essential reading for analog design engineers and researchers in the field of AD converters and it is also suitable as a text for an advanced course on the subject.

2. Record Nr.	UNISA996390378903316
Autore	Latimer Hugh <1485?-1555.>
Titolo	Fruitfull sermons [[electronic resource]] : preached by the right Reverend Father, and constant martyr of Iesus Christ, Master Hugh Latimer, newly imprinted with others not heretofore set forth in print, to the edifying of all which will dispose themselves to the reading of the same. Seene and allowed according to the order appointed in the Kings Majesties injunctions
Pubbl/distr/stampa	Printed at London, : By Thomas Cotes, for the Companie of Stationers, 1635. Cum privilegio
Descrizione fisica	[10], 225, 228-328 leaves : port
Altri autori (Persone)	BernherAugustine
Soggetti	Sermons, English - 16th century
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	A less complete collection of these was first published in 1562 as: 27 sermons. Edited by Augustine Bernher.

