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Sommario/riassunto	The proceedings of the March 1995 symposium, GLSVLSI'95, comprise 22 regular papers and 29 short papers, all refereed, in technical sessions devoted to synthesis (3 sessions), analog VLSI, physical design (3 sessions), low power design, verification, architecture and design (2 sessions), testing, asynchronous circuits, and VLSI education. Furthermore, there is a brief keynote address on 21st century gigascale integration, and the abstract of a panel on Low Power Versus High Speed: Can You Have Both? No index. Annotation copyright by Book News, Inc., Portland, OR.

