

1. Record Nr.	UNISA996279554003316
Titolo	ANSI/IEEE Std 896.1-1987 : IEEE standard backplane bus specification for multiprocessor architectures, futurebus // Institute of Electrical and Electronics Engineers
Pubbl/distr/stampa	Piscataway, New Jersey : , : IEEE, , 1988
ISBN	0-7381-4262-X
Descrizione fisica	1 online resource (61 pages)
Disciplina	004.64
Soggetti	Microcomputers - Buses
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Sommario/riassunto	This document provides a level of specification sufficient to design modules that are functionally, operationally, electrically, and mechanically compatible. Additional documents are necessary only to promote a higher degree of system level compatibility in manufacturers' products.