

1. Record Nr.	UNISA996200222903316
Titolo	Sen-I Gakkaishi []
Pubbl/distr/stampa	Tokyo-to, : Do Gakkai, Showa 19-nen [1944]-
ISSN	1884-2259
Disciplina	677.005
Soggetti	Textile fibers Textile fabrics Textile industry
Lingua di pubblicazione	Giapponese
Formato	Materiale a stampa
Livello bibliografico	Periodico
Note generali	Issues for <Oct. 1983-> have English title: Journal of the Society of Fiber and Technology, Japan; <1988/12-1989/2> Journal of the Society of Fiber Science and Technology, Japan; <1989/1-1993/10> also have English title: Fiber. Title from cover. Refereed/Peer-reviewed

2. Record Nr.	UNISA996206512903316
Titolo	Magyar közlöny : hivatalos lap
Pubbl/distr/stampa	Debrecen, : Demeter Géza, 1945-
Descrizione fisica	1 online resource
Disciplina	328.4391
Soggetti	Law - Hungary Gazettes - Hungary Gazettes Law Politics and government Municipal law Ungarn Hungary Politics and government Hungary
Lingua di pubblicazione	Hungarian
Formato	Materiale a stampa
Livello bibliografico	Periodico
Note generali	Title from caption. Published: Budapest, Apr. 22 1945- Publisher varies. Issues for <1949-1989> called: a Magyar Népköztársaság hivatalos lapja; for <1990-> a Magyar Köztársaság hivatalos lapja.

3. Record Nr.	UNINA9911019508203321
Autore	Rossetti Nazzareno <1951->
Titolo	Managing power electronics : VLSI and DSP-driven computer systems / / Nazzareno Rossetti
Pubbl/distr/stampa	Hoboken, N.J., : Wiley, c2006
ISBN	9786610287017 9781280287015 1280287012 9780470243909 0470243902 9780471776338 0471776335 9780471776291 0471776297
Edizione	[1st edition]
Descrizione fisica	1 online resource (405 p.)
Disciplina	621.381/044
Soggetti	Integrated circuits - Very large scale integration Semiconductors Signal processing - Digital techniques
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
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3.2 Elementary Circuits -- Current Mirror -- Current Source -- Differential Input Stage -- Differential to Single Input Stage -- Buffer --
 3.3 Operational Amplifier (Opamp) -- Inverting and Non-Inverting Inputs -- Rail to Rail Output Operation -- CMOS Opamp -- Opamp Symbol and Configurations -- DC Open Loop Gain -- AC Open Loop Gain -- 3.4 Voltage Reference -- Positive TC of V_{BE} -- Negative TC of V_{BE} -- Build a V_{BE} -- Building a Voltage Reference -- Fractional Band-Gap Voltage Reference -- 3.5 Voltage Regulator -- 3.6 Linear versus Switching -- 3.7 Switching Regulators -- 3.8 Buck Converters -- Switching Regulator Power Train -- Output Capacitor -- Electrolytic Capacitors and Transient Response -- Ceramic Capacitors -- Losses in the Power Train -- The Analog Modulator -- Driver -- Switching Regulator Block Diagram -- Switching Regulator Control Loop -- Input Filter -- Input Inductor LIN -- Input Capacitor -- Current Mode -- 3.9 Flyback Converters -- PART II. DIGITAL CIRCUITS -- 3.10 Logic Functions -- NAND Gate -- Set-Reset R Flip-Flop -- Current Mode with Anti-Bouncing Flip-Flop -- 4. DC-DC Conversion Architectures -- 4.1 Valley Control Architecture -- Peak and Valley Control Architectures -- Transient Response of Each System. Valley Control with FAN5093 -- Conclusion -- 4.2 Monolithic Buck Converter -- A New Design Methodology for Faster Time to Market The Design Cycle -- The FAN5301 -- The Behavioral Model -- Light Load Operation -- Full Load Operation -- Over-Current -- One Shot -- Comparator -- Results -- Timing -- Conclusion -- 4.3 Active Clamp -- Introduction -- Application -- Test Results -- Comments -- 4.4 Battery Charging Techniques: New Solutions for Notebook Battery Chargers -- High Efficiency -- The Smart Battery System -- Data Conversion -- Fast Charge -- Battery Charger System -- 4.5 Digital Power -- Control Algorithm of Modern Switching Regulators: Analog or Digital? -- Fast Switch mode Regulators and Digital Control -- 5. Offline (AC-DC) Architectures -- 5.1 Offline Power Architectures -- Introduction -- Offline Control -- PFC Architecture -- DC-DC Conversion Down to Low Voltage -- Future Trends -- 5.2 Power AC Adapter: Thermal and Electrical Design -- Introduction: The Challenge -- AC Adapter Power Dissipation -- AC Adapter Case Temperature -- Active and No-load Operation -- Development of a Solution -- Conclusion -- 6. Power Management of Ultraportable Devices -- 6.1 Power Management of Wireless Computing and Communications Devices -- The Wireless Landscape -- Power Management Technologies for Wireless -- Cellular Telephones -- Wireless Handheld -- Charge -- Protection and Fuel Gauging -- Convergence of Cellular Telephone and Handheld -- Future Architectures -- 6.2 Power Management in Wireless Telephones: Subsystem Design Requirements -- Smart Phone Subsystems -- Display Board -- Keypad Board -- Main Board -- Battery Pack -- AC Adapter -- 6.3 Powering Feature-Rich Handsets -- Growing Complexity and Shrinking Cycle Time -- Power Management Unit -- Low Dropouts (LDOs) -- 6.4 More on Power Management Units in Cell Phones -- Barriers to Up-Integration -- PMU Building Blocks -- CPU Regulator -- Low Dropout Block -- The Microcontroller -- The Microcontroller Die -- Processing Requirements. Microcontroller-Driven Illumination System -- 6.5 Color Displays and Cameras Increase Demand on Power Sources and Management -- Digital Still Camera -- Camera Phones -- Power Minimization -- Untethered Operation -- 7. Computing and Communications Systems. 7.1 Power Management of Desktop and Notebook Computers -- Power Management System Solution for a Pentium III Desktop System -- Power Management System Solution for Pentium IV Systems (Desktop and Notebook) -- Desktop Systems -- Powering the Silver Box --

Notebook Systems -- Future Power Trends -- 7.2 Computing and Data Communications Converge at the Point of Load -- The Proliferation of Power Supplies -- Telecom Power Distribution -- Computing Power Distribution -- Multiphase Buck Converter for POLs and VRMs -- Conclusion -- 7.3 Efficient Power Management ICs Tailored for DDR-SDRAM Memories -- Introduction -- DDR Power Management Architecture -- Worst Case Current Consumption -- Average Power Consumption -- Transient Operation -- Standby Operation -- Linear versus Switching -- Second Generation DDR-DDR2 -- FAN5236 for DDR and DDR2 Memories -- Future Trends -- 7.4 Power Management of Digital Set-Top Boxes -- Set-Top Box Architecture -- Power Management -- High Power Set-Top Boxes -- Low Power Set-Top Boxes -- Conclusion -- 7.5 Power Conversion for the Data Communications Market -- Introduction -- Current Environment with Separate Networks -- Migration to Converged Voice/Data/Video IP -- Telecom -48 V DC Power Distribution -- Datacom AC Power Distribution -- Conclusion -- 8. Future Directions and Special Topics -- 8.1 Beyond Productivity and Toys: Designing ICs for the Health Care Market -- 8.2 Power Management Protocols Help Save Energy -- ACPI -- Motherboard (DC-DC) Voltage Regulators -- Offline (AC-DC) Voltage Regulators with Power -- Factor Correction (PFC) -- Green Power (Energy Management) -- New Low Power System Requirements -- Conclusion -- 8.3 Heat Disposal in Electronics Applications -- Active versus Passive Cooling -- Limits of Passive Cooling -- Active Cooling -- Active Cooling-Yes or No? -- Active Cooling Implementation -- 8.4 Web Based Design Tools -- The Tools on the Web -- 8.5 Motor Drivers for Portable Electronic Appliances -- Introduction -- Camera Basics -- Motors and Motor Drivers -- Driving Implementation -- Efficiency -- DSC Power Consumption -- Conclusion -- Appendix A. Fairchild Specifications for FAN5093 -- Appendix B. Fairchild Specifications for FAN4803 -- Appendix C. Fairchild Specifications for FSD210 and FSD200 -- Appendix D. Fairchild Specifications for FAN5307 -- Appendix E. Fairchild Specifications for ACE1502 -- Appendix F. Fairchild Specifications for FAN5236 -- Appendix G. Fairchild Specifications for FAN8702 -- Glossary -- Further Reading -- Index.

Sommario/riassunto

A unique system focus that presents specific solutions for specific appliances This publication presents state-of-the-art power management techniques for modern electronic appliances that rely on such very large-scale integration (VLSI) chips as CPUs and DSPs. The author thoroughly covers all aspects of the field, including semiconductor manufacturing processes, packages, circuits, functions, and systems. A unique and significant contribution to the field, the publication adopts a "system focus" by first presenting the appliance and then delving into the power management architecture and topologies that best serve each appliance. In addition to specific techniques and applications, the publication discusses fundamental physical and socioeconomic issues. For example, the author examines Moore's law and its effect on power management and heat dissipation, which points to a future breakthrough needed to continue the fast pace of advancement in the high-tech industry. The author provides a solid technical foundation and an analysis of popular electronic appliances, including:

- * Overview of the semiconductor industry
- * Plain-English discussion of semiconductor processes and packages
- * Step-by-step guide to analog design building from the transistor to higher-level functions, leading to the implementation of a complete voltage regulator
- * Popular DC-DC voltage regulation architectures
- * AC-DC architectures for power conversion
- * Ultra-portable devices, such as

cellular phones, PDAs, and digital still cameras * Desktop and notebook PCs The publication concludes with a chapter on special power management topics and an expert forecast of future directions for the field. This is essential reading for researchers, engineers, and designers in the semiconductor and integrated circuits industries. With its extensive use of cross-section drawings as well as transistor circuit schematics, this is also a recommended textbook for advanced undergraduate and graduate courses in computer science and electrical engineering.
