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	Nota di contenuto	Next generation of system architectures for tele-immersive environments, "K Design of multimillion-gate multimedia SoCs: where do we stand?, "S Frame buffer compression using a limited- size code book for low-power display systems, "Hojun A perception- aware low-power software audio decoder for portable devices, "S A data discarding framework for reducing the energy consumption of Viterbi decoder in decoding broadcasted wireless multi-resolution JPEG2000 images, "Feng Addressing computational and networking constraints to enable video streaming from wireless appliances, "S Energy analysis of multimedia watermarking on mobile handheld devices,"A A NUCA model for embedded systems cache design,"P. Dynamic time-slot allocation for QoS enabled networks on chip,"T. Custom processor design using NISC: a case-study on DCT algorithm,"B Customizing 16-bit floating point instructions on a NIOS II processor for FPGA image and media processing,"D An integrated CAD tool for ASIC implementation of multiplierless FIR filters with common sub-expression elimination optimization,"Qiu-zhong Data-access optimization of embedded systems through selective inlining transformation, "M Operation shuffling for low energy L0 cluster generation on heterogeneous VLIW processors,"Y JPEG encoding on the Intel MXP5800: a platform-based design case study," A A component-based approach for MPSoC SW design: experience with OS customization for H.264 decoder,"A An interface for the design and implementation of dynamic applications on multi-processor

architectures, "J. -- A data oriented approach to the design of reconfigurable stream decoders, "G. -- Scratchpad sharing strategies for multiprocess embedded systems: a first approach, "M. -- Combining data and instruction memory energy optimizations for embedded applications, "T. -- Workload characterization and cost-quality tradeoffs in MPEG-4 decoding on resource-constrained devices, "Yanhong --Characterizing and exploiting task load variability and correlation for energy management in multi core systems, "S.