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Sommario/riassunto	Annotation 18th IEEE/IFIP International Workshop on Rapid System Prototyping (RSP 2007) 28-30 May 2007 / Porto Alegre, RS, Brazil RSP is a world-class conference in which participants from academia and industry aim to answer the ever pertinent question of how to model, design, test and validate better software and hardware systems in less time and with smaller cost and effort. The papers from researchers in the areas of software and hardware contribute to this natural intricate evolution of computer-based hardware and software systems. The technologies covered in RSP 2007 focus on diverse software systems, reconfigurable computing, and custom hardware applied to applications ranging from aerospace to telecommunications systems and from military to commercial systems. Contents: Co-design; HW Performance; FPGA Applications; Formal Specification; Prototyping and Development Methodologies and Tools; Testing; HW Performance, Co-Design and Tool; Applications; FPGA and SW Algorithms.