1. Record Nr.   UNICAMPANIAVAN00102557

   Autore   Helms, Lester L.

   Titolo   Potential theory / Lester L. Helms

   Pubbl/distr/stampa London, : Springer, 2014

   Titolo uniforme   Potential theory

   Edizione   [2. ed]

   Descrizione fisica XIV, 485 p. ; 24 cm

   Soggetti   31Axx - Two-dimensional potential theory [MSC 2020]
                       31Bxx - Higher-dimensional potential theory [MSC 2020]
                       31Cxx - Generalizations of potential theory [MSC 2020]
                       60Jxx - Markov processes [MSC 2020]

   Lingua di pubblicazione   Inglese

   Formato   Materiale a stampa

   Livello bibliografico   Monografia

2.

Record Nr.	UNINA9911020441003321
Autore	Fleury Martin <1951->
Titolo	Pipeline processor farms : structured design for embedded parallel systems / / M. Fleury, A.C. Downton
Pubbl/distr/stampa	New York, : Wiley, 2001
ISBN	9786610264773
	9781280264771
	1280264772
	9780471464143
	0471464147
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Descrizione fisica	1 online resource (330 p.)
Collana	Wiley series on parallel and distributed computing
Altri autori (Persone)	DowntonA. C
Disciplina	005.2/76
Soggetti	Embedded computer systems - Programming
	Parallel computers - Programming
Lingua di pubblicazione	Inglese
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Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di contenuto	Foreword; Preface; Acknowledgments; Contents; Acronyms; Part I: Introduction and Basic Concepts; 1 Introduction; 1.1 Overview; 1.2 Origins; 1.3 Amdahl's Law and Structured Parallel Design; 1.4 Introduction to PPF Systems; 1.5 Conclusions; Appendix; 2 Basic Concepts; 2.1 Pipelined Processing; 2.2 Pipeline Types; 2.3 Data Farming and Demand-based Scheduling; 2.4 Data-farm Performance Criteria; 2.5 Conclusion; Appendix; 3 PPF in Practice; 3.1 Application Overview; 3.2 Parallelization of the Postcode Recognizer; 3.3 Parallelization of the address verifier; 3.4 Meeting the Specification 3.5 ConclusionAppendix; 4 Development of PPF Applications; 4.1 Analysis Tools; 4.2 Tool Characteristics; 4.3 Development Cycle; 4.4 Conclusion; Part II: Analysis and Partitioning of Sequential Applications; 5 Initial Development of an Application; 5.1 Confidence Building; 5.2 Automatic and Semi-automatic Parallelization; 5.3 Language Proliferation; 5.4 Size of Applications; 5.5 Semi-automatic Partitioning; 5.6 Porting Code; 5.7 Checking a Decomposition; 5.8 Optimizing

Sommario/riassunto	This book outlines a methodology for the use of parallel processing in real time systems. It provides an introduction to parallel processing in general, and to embedded systems in particular. Among the embedded systems are processors in such applications as automobiles, various machinery, IPGAs (field programmable gate arrays), multimedia embedded systems such as those used in the computer game industry, and more.* Presents design and simulation tools as well as case studies.* First presentation of this material in book form.