

1. Record Nr.	UNISA996575393403316
Titolo	13213-1994 : ISO/IEC/IEEE international standard for information technology--microprocessor systems--Control and Status Registers (CSR) architecture for microcomputer buses / / Institute of Electrical and Electronics Engineers
Pubbl/distr/stampa	New York, New York : , : IEEE, , 1994
ISBN	0-7381-1214-3
Descrizione fisica	1 online resource
Disciplina	004.64
Soggetti	Microcomputers - Buses
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Sommario/riassunto	The document structure and notation are described, and the objectives and scope of the CSR Architecture are outlined. Transition set requirements, node addressing, node architectures, unit architectures, and CSR definitions are set forth. The ROM specification and bus standard requirements are covered.

2. Record Nr.	UNINA9911020089403321
Autore	Hochstadt Harry
Titolo	Integral Equations
Pubbl/distr/stampa	[Place of publication not identified], : Wiley, 1973
ISBN	1-118-16594-2
Collana	Pure and applied mathematics Integral equations.
Disciplina	515/.45
Soggetti	Civil & Environmental Engineering Engineering & Applied Sciences Operations Research Integral equations
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Bibliographic Level Mode of Issuance: Monograph