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Autore	Esolen Anthony M.
Titolo	Out of the Ashes : An Oral History of the Provisional Irish Republican Movement
Pubbl/distr/stampa	Irish Academic Press
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Formato	Musica
Livello bibliografico	Monografia
Sommario/riassunto	It's not your imagination: civilized human society is collapsing. Communities no longer work towards a common good; children are no longer our first priority; businesses no longer value "hard work"; arts and skills have been lost; and gender is decided by the individual, not biology. We cannot reverse national and global trends, says professor Anthony Esolen; but we can build communities that live up to humanity's promise and responsibility. In Out of the Ashes, Esolen identifies the gaping problems in our society and lays out a blueprint for reconstruction that puts our future in the hands of individuals focused on the good of the local community.

2. Record Nr.	UNINA9911018758603321
Autore	Gerfers F (Friedel)
Titolo	Bulk-Driven Circuit Techniques for CMOS FDSOI Processes : From Circuit Concept to Implementations // edited by Friedel Gerfers
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Descrizione fisica	1 online resource (357 pages)
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Soggetti	Electronic circuit design Embedded computer systems Materials Electronics Design and Verification Embedded Systems Materials for Devices
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Nota di contenuto	Chapter 1. Common-Source Amplifier Feedback using Back-Gate Transconductance -- Chapter 2. Transconductance Amplifier Linearization using Active Back-Gate Input Signal Injection -- Chapter 3. A Gain-enhanced Inverter-based OTA Employing Active Body-Bias Feedback -- Chapter 4. Ultra Low-Power Voltage-Mode VCSEL-Driver with Back-Gate Bias Tuning -- Chapter 5. Maximizing the Figures of Merit, Temperature Range, and Optimizing the Algorithmic Design Methodologies Based on Constant Current Density Bias for FDSOI Analog-Mixed-Signal, Digital, mm-Wave, and Fibreoptic Circuits from the Back-Gate Voltage -- Chapter 6. A Back-Gate Linearization Technique for Current-Steering DACs -- Chapter 7. Highly-Linear T&H-Amplifiers Utilizing Bandwidth Boosting for Time-Interleaved ADCs -- Chapter 8. High-Speed Flash ADC using Bulk-Driven Flash Reference Generation Technique -- Chapter 9. RF Switches in CMOS FDSOI Process – from Circuit Concepts to Implementation -- Chapter 10. Variable Gain-Control with Bulk Biasing in mmW Amplifier.
Sommario/riassunto	In the contemporary technology landscape dominated by digital-centric systems and applications, the significance of analog front-end signal

processing remains indispensable. The precision and performance of critical analog, mixed-signal or mm-wave components such as low-noise amplifiers, equalizers, and data converters are fundamentally determined by technological parameters, such as transconductance, DC gain, device matching, linearity, and timing accuracy, among others. Enhancing these parameters through intrinsic design improvements presents a significant challenge and becomes infeasible beyond certain limits with state-of-the-art circuit design techniques. As the performance of CMOS transistors is fundamentally constrained, foreground or background calibration schemes are commonly employed to mitigate the limitations of MOS devices. However, these constraints can be effectively addressed through the implementation of active and passive bulk-driven circuits enabled by silicon-on-insulator (SOI) CMOS technologies. Fully-Depleted Silicon-on-Insulator (FD-SOI) CMOS technologies offer superior transistor characteristics compared to standard bulk CMOS technology, providing enhanced electrical performance, improved power efficiency, and better scalability. This book offers a comprehensive analysis of FD-SOI CMOS technology, presenting key innovations in design methodologies and circuit implementations adopting bulk-biasing techniques across analog, digital, mixed-signal, and mmWave circuits and systems. It addresses critical transistor limitations, including finite transistor gain, offset, mismatch, noise and linearity, among others. The authors provide detailed technical insights, mathematical modelling, design approaches and circuit realizations covering circuit advances using both static and dynamic transistor body-biasing techniques. Emphasis is placed on overcoming state-of-the-art circuit limitations such as finite DC gain, bandwidth, matching/accuracy and power efficiency. These performance metrics are rigorously investigated through mathematical modelling, validated through simulation and experimentally demonstrated using both dynamic and static body-biasing architectures. An essential guide for innovations using dynamic and static transistor body-biasing techniques; Describes FD-SOI CMOS bulk physics incl. the impact on technology parameters; Presents advanced active and passive body-biasing design methods.
