1. Record Nr. UNINA9911007491603321 Autore Meyer-Baese Uwe **Titolo** Embedded Microprocessor System Design using FPGAs / / by Uwe Meyer-Baese Cham:,: Springer Nature Switzerland:,: Imprint: Springer,, 2025 Pubbl/distr/stampa **ISBN** 3-031-82822-4 Edizione [2nd ed. 2025.] 1 online resource (1033 pages) Descrizione fisica Disciplina 621.3916 Soggetti Embedded computer systems Electronic circuit design Cooperating objects (Computer systems) **Embedded Systems Electronics Design and Verification** Cyber-Physical Systems Lingua di pubblicazione Inglese **Formato** Materiale a stampa Livello bibliografico Monografia Embedded Microprocessor Systems Basics -- FPGA Devices, Boards. Nota di contenuto and Design Tools -- Microprocessor Component Design in VHDL --Microprocessor Component Design in Verilog -- Microprocessor Programming in C/C++ -- Software Tools for Embedded Microprocessors Systems -- Design of the PicoBlaze Softcore Microprocessor -- Software tools for the PicoBlaze Softcore Microprocessor -- Altera/Intel Nios Embedded Microprocessor -- Xilinx MicroBlaze Embedded Microprocessor -- ARMv7 Cortex-A9 Embedded Microprocessor. Sommario/riassunto This textbook for courses in Embedded Systems introduces students to necessary concepts, through a hands-on approach. It gives a great introduction to FPGA-based microprocessor system design using stateof-the-art boards, tools, and microprocessors from Altera/Intel® and Xilinx®. HDL-based designs (soft-core), parameterized cores (Nios II and MicroBlaze), and ARM Cortex-A9 design are discussed, compared and explored using many hand-on designs projects. Custom IP for

HDMI coder, Floating-point operations, and FFT bit-swap are developed, implemented, tested and speed-up is measured. New

additions in the second edition include bottom-up and top-down FPGA-based Linux OS system designs for Altera/Intel® and Xilinx® boards and application development running on the OS using modern popular programming languages: Python, Java, and JavaScript/HTML/CSSs. Downloadable files include all design examples such as basic processor synthesizable code for Xilinx and Altera tools for PicoBlaze, MicroBlaze, Nios II and ARMv7 architectures in VHDL and Verilog code, as well as the custom IP projects. For the three new OS enabled programing languages a substantial number of examples ranging from basic math and networking to image processing and video animations are provided. Each Chapter has a substantial number of short quiz questions, exercises, and challenging projects. Explains soft, parameterized, and hard core systems design tradeoffs; Demonstrates design of popular KCPSM6 8 Bit microprocessor step-bystep; Discusses the 32 Bit ARM Cortex-A9 and a basic processor is synthesized; App design in Python, Java, and JavaScript/HTML/CSS for FPGA boards...

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Autore Stavetski Edward J. <1956->

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Nota di bibliografia Includes bibliographical references and index. Nota di contenuto Asset allocation and fiduciary duty -- Large versus small funds -- The search for an honest man -- Performance analysis -- Risk in hedge funds -- You only find out who is swimming naked when the tide goes out -- Let the games begin -- Getting ready is the secret to success --Navigating buyers remorse -- Monitoring your flock. Invaluable insight into measuring the performance of today's hedge Sommario/riassunto fund managerMore and more institutional funds and high-net-worth assets are finding their way to hedge funds. This book provides the quantitative and qualitative measures and analysis that investment managers, investment advisors, and fund of fund managers need to allocate and monitor their client's assets properly. It addresses important topics such as Modern Portfolio Theory (MPT) and Post Modern Portfolio Theory (PMPT), choosing managers, watching

performance, and researching alternate asset classes. Author Edward St