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Nota di contenuto	Contents; Chapter 1. Transient and Frequency Response; Reactive Circuit Elements; First-Order Time-Domain Transient Response; Complex Poles and the Complex Frequency Domain; Second-Order Time-Domain Response: RLC Circuit; Forced Response and Transfer Functions in the s-Domain; The Laplace Transform; Time-Domain Response to a Unit Step Function; Circuit Characterization in the Time Domain; The s-Plane Frequency Response of Transfer Functions; Graphical Representation of Frequency Response; Loci of Quadratic Poles; Optimization of Time-Domain and Frequency-Domain Response Reactance Chart Transfer Functions of Passive CircuitsClosure; Chapter 2. Dynamic Response Compensation; Passive Compensation: Voltage Divider; Op-Amp Transfer Functions from Reactance Charts; Feedback Circuit Response Representation; Feedback Circuit Stability; Compensation Techniques; Compensator Design: Compensating with Zeros in H; Compensator Design: Reducing Static Loop Gain; Compensator Design: Pole Separation and Parameter Variation; Two-Pole Compensation; Output Load Isolation; Complex Pole Compensation; Compensation by the Direct (Truxal's) Method; Power Supply Bypassing Chapter 3. High-Frequency Impedance TransformationsActive Device Behavior above Bandwidth; BJT High-Frequency Model; Impedance

Transformations in the High-Frequency Region; Reactance Chart Representation of b-Gyrated Circuits; Reactance Chart Stability Criteria for Resonances; Emitter-Follower Reactance-Plot Stability Analysis; Emitter-Follower High-Frequency Equivalent Circuit; Emitter-Follower High-Frequency Compensation; Emitter-Follower Resonance Analysis from the Base Circuit; Emitter-Follower Compensation with a Base Series RC; BJT Amplifier with Base Inductance

Sommario/riassunto

This second volume, Designing Dynamic Circuit Response builds upon the first volume Designing Amplifier Circuits by extending coverage to include reactances and their time- and frequency-related behavioral consequences.
