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Descrizione fisica	1 online resource (viii, 239 pages) : digital, PDF file(s)
Altri autori (Persone)	LevantinoSalvatore SamoriCarlo
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Nota di contenuto	Local oscillator requirements -- Phase-locked loops -- Fractional-N PLLs -- Electronic oscillators -- Noise in oscillators -- Reactive components in oscillators -- Noise up-conversion in VCOs -- Frequency division -- Phase comparison.
Sommario/riassunto	The increasingly demanding performance requirements of communications systems, as well as problems posed by the continued scaling of silicon technology, present numerous challenges for the design of frequency synthesizers in modern transceivers. This book contains everything you need to know for the efficient design of frequency synthesizers for today's communications applications. If you need to optimize performance and minimize design time, you will find

this book invaluable. Using an intuitive yet rigorous approach, the authors describe simple analytical methods for the design of phase locked loop (PLL) frequency synthesizers using scaled silicon CMOS and bipolar technologies. The entire design process, from system-level specification to layout, is covered comprehensively. Practical design examples are included, and implementation issues are addressed. A key problem-solving resource for practitioners in IC design, the book will also be of interest to researchers and graduate students in electrical engineering.
