

1. Record Nr.	UNINA9910153063003321
Autore	Parrillo Vincent N.
Titolo	Understanding race and ethnic relations // Vincent N. Parrillo
Pubbl/distr/stampa	Harlow, Essex, England : , : Pearson, , [2014] ©2014
ISBN	1-292-05479-4
Edizione	[Fourth edition, Pearson new international edition.]
Descrizione fisica	1 online resource (184 pages) : illustrations, tables
Collana	Pearson custom library
Disciplina	305.8
Soggetti	Race relations Ethnic relations Minorities United States Race relations United States Ethnic relations
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Includes index.
Nota di bibliografia	Includes bibliographical references (pages 187-202) and index.
Nota di contenuto	Cover -- Table of Contents -- 1. Appendix: Immigration, 1820-2009 -- 2. The Study of Minorities -- 3. The Role of Culture -- 4. Ethnic and Racial Stratification -- 5. Prejudice -- 6. Discrimination -- 7. Dominant-Minority Responses -- Index -- 9.
Sommario/riassunto	For undergraduate and graduate introductory level courses in race and ethnic relations. Introducing the core theories, concepts, and issues concerning race and ethnic relations in the United States. Based on the top-selling title by the same author, Strangers to These Shores, this book provides a framework for understanding the interpersonal dynamics and the larger context of changing intergroup relations. Following a presentation of introductory concepts in the first chapter-particularly that of the stranger as a social phenomenon and the concept of the Dillingham Flaw-the first group of chapters examines differences in culture, reality perceptions, social class, and power as reasons for intergroup conflict. These chapters also look at the dominant group's varying expectations about how minorities should "fit" into its society. Chapters 2 and 3 include coverage of some middle-range conflict and interactionist theories. Chapters 4 and 5 explore the dimensions and interrelationships of prejudice and

discrimination, and Chapter 6 covers the dominant-minority response patterns so common across different groups and time periods. This chapter presents middle-range conflict theories about economic exploitation too. Chapter 7 employs holistic sociological concepts in discussing ethnic consciousness; ethnicity as a social process; current racial and ethnic issues, fears, and reactions; and the various indicators of U.S. diversity in the 21st century. &nbsp;

2. Record Nr.	UNINA9910300384903321
Autore	Giesecke Johannes
Titolo	Quantitative Recombination and Transport Properties in Silicon from Dynamic Luminescence // by Johannes Giesecke
Pubbl/distr/stampa	Cham : , : Springer International Publishing : , : Imprint : Springer, , 2014
ISBN	3-319-06157-7
Edizione	[1st ed. 2014.]
Descrizione fisica	1 online resource (296 p.)
Collana	Springer Theses, Recognizing Outstanding Ph.D. Research, , 2190-5053
Disciplina	537.6226
Soggetti	Semiconductors Energy systems Materials—Surfaces Thin films Renewable energy resources Energy Systems Surfaces and Interfaces, Thin Films Renewable and Green Energy
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references.
Nota di contenuto	Introduction -- Solar Cell Operation -- Dynamics of Charge Carriers -- Luminescence of Silicon -- Harmonically Modulated Lifetime -- Constraints of Dynamic Carrier Lifetime Techniques -- Evolution of the Experimental Setup -- Conceptual Advances: Recombination Properties -- Conceptual Advances: Transport Properties -- Summary and

## Outlook.

### Sommario/riassunto

Harmonically modulated luminescence combines the advantages of highly sensitive luminescence metrology with an immediate dynamic access to carrier lifetime in semiconductors at a minimum of required a priori information. The present work covers theoretical, conceptual, and experimental advances of the harmonically modulated luminescence technique. Theoretical constraints of dynamic carrier lifetime techniques are rigorously elaborated, including the proof of their differential nature and their characteristics at nonuniform spatial distributions of recombination rate. The pathway toward a unified, reliable, and versatile harmonically modulated carrier lifetime metrology is delineated - covering the entire solar cell production chain from bare ingots to finished solar cells. Accurate access to miscellaneous relevant recombination and transport properties via harmonically modulated luminescence is demonstrated and experimentally validated, embracing injection-dependent carrier lifetimes at extremely low injection conditions, a spatially resolved carrier lifetime calibration of luminescence images, and accurate approaches to both net dopant concentration and minority carrier mobility.

3. Record Nr.	UNINA9910993927803321
Autore	Chakravarthi Veena S
Titolo	SOC-Based Solutions in Emerging Application Domains // by Veena S. Chakravarthi, Shivananda R. Koteswar
Pubbl/distr/stampa	Cham : , : Springer Nature Switzerland : , : Imprint : Springer, , 2025
ISBN	3-031-85044-0
Edizione	[1st ed. 2025.]
Descrizione fisica	1 online resource (XXVI, 185 p. 36 illus., 34 illus. in color.)
Disciplina	621.3815
Soggetti	Electronic circuits Embedded computer systems Electronics Electronic circuit design Electrical engineering Electronic Circuits and Systems Embedded Systems Electronics and Microelectronics, Instrumentation Electronics Design and Verification Electrical and Electronic Engineering
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di contenuto	Chapter 1 Advanced Process Technologies of for designing SOC's -- Chapter 2 Artificial Intelligent SOC's:AI SOC's -- Chapter 3 Software Defined SOC's -- Chapter 4 Designing Three-dimensional SOC's -- Chapter 5 Designing SOC's with enhanced security and ZTS principles -- Chapter 6 Network on Chips -- Chapter 7 Application specific Instruction Set Processors -- Chapter 8 Quantum-System on chips -- Chapter 9 Designing Photonic SOC -- Chapter 10 RISC-V Based Processor System -- Chapter 11 Planning a Successful SOC: Activities beyond actual Design -- Chapter 12 Impact of Adoption of AI in SOC Design flow.
Sommario/riassunto	Working in the ever-evolving field of smart chip design within an AI-powered design environment, the authors of this book draw on their experiences in successfully developing system-on-chip (SoC) solutions,

having grappled with the emerging design environment, innovative tools, domain-specific challenges, and major design decisions for SOC-based solutions. They present the first comprehensive guide to navigating the technical challenges of SOC-based solutions in emerging application domains, covering various design and development methodologies for system-on-chip solutions for emerging target applications. When diligently applied, the strategies and tactics presented can significantly shorten development timelines, help avoid common pitfalls, and improve the odds of success, especially in AI-powered smart EDA environments. The book provides a detailed insight into SoC-based solutions for various applications, including artificial intelligence (AI), post-quantum security feature enhancements, 3D SOC, quantum SOC, photonic SOC, and SOC solutions for IoT, high-performance computing SOC, and processor-based systems. The coverage includes architecture exploration methods for targeted applications, compute-intensive SoCs, lightweight SoCs for IOT applications, advanced technology node solutions, and solutions including hardware software co-designs and software-defined SoCs. The strategies best applied in these highly advanced technology developments are discussed in a guest chapter by a practicing high technology strategist so innovators, designers, entrepreneurs, product managers, investors, and executives may properly prepare their companies to succeed. Offers a collection of design solutions for emerging applications and EDA environments; <Clearly explains the AI-powered design environment for faster turnaround with domain-specific challenges addressed; Discusses the methodologies used for advanced System-on-Chip (SoC) design for high technology emerging applications.

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