

1. Record Nr.	UNISA996396241503316
Autore	Parsons Robert <1546-1610.>
Titolo	A relation of the triall made before the King of France, vpon the yeare 1600 betvveene the Bishop of Eureux, and the L. Plessis Mornay [[electronic resource]] : About certayne pointes of corrupting and falsifying authors, wheroft the said Plessis was openly conuicted. Newly reuewed, and sett forth againe, with a defence therof, against the impugnations both of the L. Plessis in France, & of O.E. in England. By N.D
Pubbl/distr/stampa	[Saint-Omer, : F. Bellet], Imprinted vwith licence. Anno M. DC. IIII. [1604]
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Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	N.D. = N. Doleman, i.e. Robert Parsons. Place of publication and printer's name from STC. Also issued as part 3 of STC 19416: Parsons, Robert. A treatise of three conversions of England. Reproduction of the original in the Bodleian Library.
Sommario/riassunto	eebo-0014

2. Record Nr.	UNINA9910983373103321
Autore	Guerrero Ginés
Titolo	High Performance Computing : 11th Latin American High Performance Computing Conference, CARLA 2024, Santiago de Chile, Chile, September 30 – October 4, 2024, Revised Selected Papers / / edited by Ginés Guerrero, Jaime San Martín, Esteban Meneses, Carlos Jaime Barrios Hernández, Carla Osthoff, Jose M. Monsalve Diaz
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Altri autori (Persone)	San MartinJaime MenesesEsteban Barrios HernandezCarlos Jaime OsthoffCarla Monsalve DiazJose M
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Soggetti	Computer engineering Computer networks Artificial intelligence Social sciences - Data processing Microprogramming Computer input-output equipment Computer Engineering and Networks Artificial Intelligence Computer Application in Social and Behavioral Sciences Control Structures and Microprogramming Input/Output and Data Communications
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Livello bibliografico	Monografia
Nota di contenuto	-- High Performance Computing Track. -- Impact of job scheduling policy changes on user behaviour and system response: The case of the

Santos Dumont supercomputer in Brazil. -- A Study of Performance Portability in Plasma Physics Simulations. -- High performance computing for auto supervised machine learning training: parallel-distributed implementation of the Word2Vec algorithm for training word embeddings. -- A Comprehensive Analysis of Process Energy Consumption on Multi-Socket Systems with GPUs. -- Enhancing Reverse Time Migration Simulations in HPC Systems through I/O and Computation Overlapping. -- Evaluation of Computational and Power Performance in Matrix Multiplication Libraries - MKL Vs cuBLAS. -- A User-centric Evaluation Methodology for Informed Provisioning of High Performance Computing Resources in Academic Institutions. -- EfiMon: A Process Analyser for Granular Power Consumption Prediction. -- Leveraging CPU-FPGA Co-design for Matrix Profile Computation. -- Artificial Intelligence at HPC Scale Track. -- Web system for recognizing actions of physical violence in urban spaces using CNN with Transfer Learning. -- Quantized SG-MCMC for Bayesian Deep Posterior Compression. -- No Plankton Left Behind: Preliminary results on massive plankton image recognition. -- Machine Learning Regression-based Prediction for Improving Performance and Energy Consumption in HPC platforms. -- A new computational framework for crop yield estimation and phenological monitoring. -- Histopathology Image Augmentation through StyleGAN2-ADA. -- High Performance Computing Applications Track. -- Strategies to Reduce Memory Consumption in Software Quantum Computing Simulators. -- Adaptive Edge-Based IoT Architecture for Efficient Retraining and Sustainable Monitoring of Ephemeral Streams. -- Multi-GPU Tomographic Reconstructions of Large Volumes in the Frequency Domain. -- Accelerating tomographic artifact removal using a multi-GPU system. -- A parallel multi-threading global energy balance for a room thermal analysis in an unsteady state. -- Parallel Computing Strategies in WRF: The Role of MPI, OpenMP, & NUMA Affinity.

Sommario/riassunto

This book constitutes the refereed revised selected papers of the 11th Latin American Conference on High Performance Computing, CARLA 2024, held in Santiago de Chile, Chile, during September 30–October 4, 2024. The 21 full papers included in this book were carefully reviewed and selected from 42 submissions. They were organized in topical sections as follows: High Performance Computing Track; Artificial Intelligence at HPC Scale Track; High Performance Computing Applications Track.
