1. Record Nr. UNISA996386448003316

Autore	Wells John <1623-1676.>
Titolo	A prospect of eternity [[electronic resource]] : or Mans everlasting condition opened and applyed. By John Wells Master of Arts, sometimes Fellow of St. Johns Colledge in Oxford, and now Pastour of Olaves Jewry LONDON
Pubbl/distr/stampa	London, : Printed by E.C. for Joseph Cranford at the Phoenix in St. Pauls-church-yard, 1655. [i.e. 1654]
Descrizione fisica	[24], 409, [5] p
Soggetti	Eternity
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Imprimatur leaf dated Septemb. 8. 1654. With two final advertisement leaves. Annotation on Thomason copy: "Octob. 10. 1654"; the second 5 in the imprint date has been crossed out. Reproduction of the original in the British Library.
Sommario/riassunto	eebo-0018

2. Record Nr. UNINA9910983355703321

Autore	Graf Susanne
Titolo	Real Time and Such : Essays Dedicated to Wang Yi to Celebrate His Scientific Career / / edited by Susanne Graf, Paul Pettersson, Bernhard Steffen
Pubbl/distr/stampa	Cham : , : Springer Nature Switzerland : , : Imprint : Springer, , 2025
ISBN	3-031-73751-2
Edizione	[1st ed. 2025.]
Descrizione fisica	1 online resource (175 pages)
Collana	Lecture Notes in Computer Science, , 1611-3349 ; ; 15230
Altri autori (Persone)	PetterssonPaul SteffenBernhard
Disciplina	005.1
Soggetti	Software engineering Machine theory Software Engineering Formal Languages and Automata Theory
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Nota di contenuto	All About Time -- Wang@MDU in a nutshell -- To Sifu - Supervision, Mentorship and Lifelong Bond -- Research in one Area Benefits Another -- Verifying PLC-Automata against Counterexample Formulas using Timed Automata -- Nudging Strategies for User Journeys: Take a Path on the Wild Side -- Adaptive Task Planning and Formal Control Synthesis using Temporal Logic Trees -- Trading Space for Simplicity in Stateless Model Checking -- Performance Analysis of Stochastic Digraph Real-Time Task Model -- A Closer Look at Pseudo-Polynomial Time and its Use in Real-Time Scheduling Theory -- Voting-Based Shortcuts through Random Forests for Obtaining Explainable Models -- Reminiscences of a Real-Time Researcher.
Sommario/riassunto	This Festschrift reflects Professor Wang Yi's contributions to the fields of formal methods, real-time systems and scheduling, and multicore systems. Wang Yi received a PhD in Computer Science from Chalmers University of Technology in 1991, since 2000 he has been Chair in Embedded Systems at Uppsala University. He has not only pushed the boundaries of theoretical research but also pioneered practical implementations in software tools that have had a profound impact on

both academia and industry. He codeveloped the UPPAAL tool, the foremost system for verifying timed automata, now widely used in both academia and industry. Over the years he expanded his research to include scheduling theories, and he developed the TIMES and TIMES-Pro tools, which enhanced the analysis and implementation of real-time systems. His innovative work has significantly influenced the design and verification of complex, multicore real-time systems. Among many awards, honours, and responsibilities, Wang received a grant from the Knut and Alice Wallenberg Foundation, an ERC Advanced Grant from the European Research Council in 2019, Uppsala University's Rudbeck Medal, the IEEE TCRTS Award for technical achievement and leadership in real-time computing, and the CAV Award; he is a Fellow of the ACM and the IEEE, and a member of the Royal Society of Sciences in Uppsala and the Academia Europaea; and he has chaired major software engineering and embedded system conferences and served on ACM SIGBED and IEEE TCRTS executive committees. His guidance and mentorship have shaped the careers of many researchers and professionals in the field, and the contributions in this volume celebrate his enduring impact.