

1. Record Nr.	UNINA9910965075403321
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Titolo	Analog circuit design for communication SOC // by Steve Hung-Lung Tu, Rong-Jyi Yang, Ding-Lan Shen
Pubbl/distr/stampa	[Taiwan], : Bentham Science Publishers, [2012]
ISBN	9781608050376 1608050378
Edizione	[1st ed.]
Descrizione fisica	1 online resource (238 p.)
Altri autori (Persone)	YangRong-Jyi ShenDing-Lan
Disciplina	621.3815
Soggetti	Electronic analog computers - Circuits Communication - Network analysis
Lingua di pubblicazione	Inglese
Formato	Materiale a stampa
Livello bibliografico	Monografia
Note generali	Description based upon print version of record.
Nota di bibliografia	Includes bibliographical references and index.
Nota di contenuto	01 Title.pdf; 02 Cover Page; 03 REVISED eBooks End User License Agreement-Website; 04 Contents; 06 Preface; 07 List of contributors; 08 Chapter 01; 09 Chapter 02; 10 Chapter 03; 11 Chapter 04; 12 Chapter 05; 13 Chapter 06; 14 Chapter 07; 15 Chapter 08; 16 Subject-index
Sommario/riassunto	This e-book provides several state-of-the-art analog circuit design techniques. It presents both empirical and theoretical materials for system-on-a-chip (SOC) circuit design. Fundamental communication concepts are used to explain a variety of topics including data conversion (ADC, DAC, S-? oversampling data converters), clock data recovery, phase-locked loops for system timing synthesis, supply voltage regulation, power amplifier design, and mixer design. This is an excellent reference book for both circuit designers and researchers who are interested in the field of design of analog communic